

Faculty Profile



1. Name: **Dr. Rajendra V. Babar**
2. Designation: HOD IT Dept., SIT, Lonavala
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4. Mobile: +91-9423558020, 7588048922
5. Date of Birth: 10th Jan 1978
6. Address: 2BHK-B, Flat No.04, STES Campus , Lonavala
7. Research Interests: Software Engineering, Wireless Communication

Educational Details:

- Ph.D. (Electronics Engg.), RTMU, Nagpur University.
- M.E. (E&TC- VLSI and Embedded Systems), Pune University.
- B.E. (E&TC), Shivaji University, Kolhapur.

Experience:

- July 2006 to Sept.2021 : - Asst. Prof., E&TC Dept., at Sinhgad Institute of Technology, Lonavala, India
- Sept.2021 to till:- HOD, IT Dept. at Sinhgad Institute of Technology, Lonavala, India

Academic Recognition

- ✓ Recognized as PG Teacher of SPPU, Pune University.

Subjects Taught:

- | UG | PG |
|---|---|
| <ul style="list-style-type: none">• Software Engineering• VLSI Design• Digital Electronics• Microprocessor and Interfacing Techniques• Advanced Processors• Analog Communication• Broadband communication Systems | <ul style="list-style-type: none">System on ChipASIC DesignReconfigurable ComputingFault Tolerant System |

Responsibilities Handled:

- NAAC Criteria-VI Institute Level Co-ordinator
- IQAC Member
- PG Co-ordinator – Dept. and College Level
- Project Co-ordinator- Department
- Session-chair for IEEE Conference GCWCN2014
- NBA Criteria Head
- IEI Students Chapter Co-ordinator.
- SPOC Mentorbuddy, Dept., SIT, Lonavala.
- ARIIIA Co-ordinator- College level
- Member of Purchase committee.
- Asst. Chairman of PG admission Committee.
- Co-ordinator for STTP, FDP, Workshop, Seminar.
- Member of Techtonic, Cultural event.
- Expert for ePGPEX, SPPU, conference of PG Course.
- Judge for many Technical events.

Research publications:

- Patent: 01
- International Journals:40, International Conference:21, National Conference:11

- **Professional Membership:**

Life Member of International Society for Technical Education. (LMISTE)

Life Member of IETE.

Books Published:

- International Book in 8 International Languages: 01 (Online)

Achievements and Awards:

- Conferred with “**Global Edu-Tech Leadership Award**” by World Education Congress, Mumbai.
- Reviewer of IEEE Transactions.
- Reviewer of International Conferences.
- Received **Bharat Vikas Award-2017** from ISR, Bhubaneswar.
- Completed Research funding Project of Savitribai Phule Pune University, Pune.
- Resource person for STTP, Seminar, Workshop, FDP.
- Subject Chairman and Paper setters of University of Pune, Solapur University and Autonomous Colleges in Pune University for UG and PG Course.
- Delivered many Guest Lectures.
- Received Appreciation letters for Good results in different subjects.



Place: Lonavala

Dr. Rajendra V. Babar