



SPOKEN TUTORIAL



Sinhgad Institutes

Certificate

OF RECOGNITION



This certificate is presented to

Mr./Ms. SANJAY WAYKAR

Of SIT, LONAVALA

has attended the ONE WEEK "FACULTY DEVELOPMENT PROGRAM" on JAVA Programming organized by Department of Computer Engineering and Information Technology, in association with Spoken Tutorial , IIT Bombay. at SKN Sinhgad Institute of Technology and Science, Lonavala during 22nd Aug 2016 to 26th Aug 2016

Prof. M. R. Gosavi
(Co-convener)

Prof. A. C. Manepatil
(Co-convener)

Prof. Vikas Thombre
Head of Department
(Convener)

Dr. M. S. Rohokale
Principal
(Chairman)



STES's

SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Department of Electrical Engineering

PRESENTS

One week STTP on "Advanced Optimization Techniques with applications in Electrical Engineering"

CERTIFICATE OF PARTICIPATION

This is to certify that **Ms. Jadhav Supriya Bhushan** of **IT dept, SIT, Lonavala** has participated in One Week STTP on **"Advanced Optimization Techniques with applications in Electrical Engineering"** Organized by Department of Electrical Engineering SIT, Lonavala during Of March 06-10, 2016.

Dr.A.A. Kalage
Conveyer
Electrical Dept. SIT Lonavala

Dr. M. S. Gaikwad
Principal, SIT Lonavala
Campus Director Lonavala



Pimpri Chinchwad Education Trust's
**PIMPRI CHINCHWAD COLLEGE
OF ENGINEERING**
(ISO Certified, NBA Accredited)




DEPARTMENT OF COMPUTER ENGINEERING


One Week Short Term Training Program
On

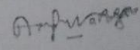
"OPTIMIZATION IN MACHINE LEARNING RESEARCH"

Certificate

This is to certify that Mr./Mrs. Vandana P. Tonde
has attended the One Week Short Term Training Program on "Optimization in
Machine Learning Research", held from 16th to 21st January, 2017 under Special
Interest Group "Data Science & Knowledge Engineering", in the Department of
Computer Engineering, Pimpri Chinchwad College of Engineering, Pune.


Dr. A. D. Thakare
Convenor


Dr. K. Rajeswari
Head of Department


Dr. A. M. Fulambarkar
Principal



One week National Level

Short Term Training Program

On

RESEARCH AND ANALYTICAL APPROACH BEYOND CMOS

"RAAC-2017"

[June 12-16, 2017]

Organized by

Industry Institute Interaction Cell [IIC]

&


Department of Electronics and Telecommunication Engineering


Sinhgad Institute of Technology, Lonavala

Certificate

This is to certify that ~~Mr. /Mrs. /Dr.~~ **VIJAYA GOPALRAO RAJESHWARKAR** has participated in Short Term Training Program on "Research and Analytical Approach beyond CMOS" [RAAC-2017], held at Sinhgad Institute of Technology, Lonavala during June, 12-16, 2017


Prof. Mrs. S.S. Bhardwaj
Coordinator


Prof. V.V. Deotare
Head of Department


Dr. M.S. Gaikwad
Principal

STES's
SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Department of Electronics & Telecommunication Engineering

PRESENTS

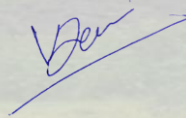
One week FDP on "Foundation of Fuzzy Set and Fuzzy Logic with Applications"

CERTIFICATE OF PARTICIPATION

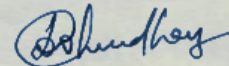
This is to certify that **Mr. Patil Deepak Balaso** of **E&Tc dept, SIT, Lonavala** has participated in One Week Faculty Development Program on **"Foundation of Fuzzy Set and Fuzzy Logic with Applications"** Organized by Department of Electronics and Telecommunication Engineering, SIT, Lonavala during October 04-08, 2016.



Prof. R.V. Babar
Co-ordinator
E&TC Dept. SIT Lonavala



Prof. V.V. Deotare
HOD
E&TC Dept. SIT Lonavala



Dr. D. D. Chaudhary
Vice Principal,
SIT Lonavala



Dr. M. S. Gaikwad
Principal, SIT Lonavala

STES's
SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Department of Electronics & Telecommunication Engineering

PRESENTS

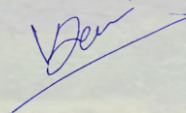
One week FDP on "Foundation of Fuzzy Set and Fuzzy Logic with Applications"

CERTIFICATE OF PARTICIPATION

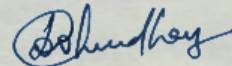
This is to certify that **Mr. Chavan Vikram Madhukar** of **E&Tc dept. SIT, Lonavala** has participated in One Week Faculty Development Program on *"Foundation of Fuzzy Set and Fuzzy Logic with Applications"* Organized by Department of Electronics and Telecommunication Engineering, SIT, Lonavala during October 04-08, 2016.



Prof. R.V. Babar
Co-ordinator
E&TC Dept. SIT Lonavala



Prof. V.V. Deotare
HOD
E&TC Dept. SIT Lonavala



Dr. D. D. Chaudhary
Vice Principal,
SIT Lonavala



Dr. M. S. Gaikwad
Principal, SIT Lonavala

STES's
SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Department of Electronics & Telecommunication Engineering

PRESENTS

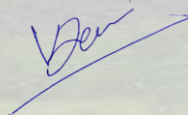
One week FDP on "Foundation of Fuzzy Set and Fuzzy Logic with Applications"

CERTIFICATE OF PARTICIPATION

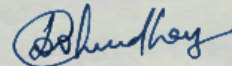
This is to certify that **Ms. Baste Vaishali Sandeep** of **E&Tc dept, SIT, Lonavala** has participated in One Week Faculty Development Program on **"Foundation of Fuzzy Set and Fuzzy Logic with Applications"** Organized by Department of Electronics and Telecommunication Engineering, SIT, Lonavala during October 04-08, 2016.



Prof. R.V. Babar
Co-ordinator
E&TC Dept. SIT Lonavala



Prof. V.V. Deotare
HOD
E&TC Dept. SIT Lonavala



Dr. D. D. Chaudhary
Vice Principal,
SIT Lonavala



Dr. M. S. Gaikwad
Principal, SIT Lonavala



Sinhgad Institutes

Sinhgad Technical Education Society's
SMT. KASHIBAI NAVALE COLLEGE OF ENGINEERING,
VADGAON (BK), Pune -411041

Department of Computer Engineering
TRAIN THE TRAINER (SE)
CERTIFICATE OF PARTICIPATION

THIS IS TO CERTIFY THAT Dr. S. D. Babar OF

S I T HAS ATTENDED TRAIN THE TRAINER WORKSHOP FOR

THE SUBJECT DSA ORGANIZED FROM 5TH TO 10TH JUNE 2017.

Prof. V. V. Jog
Coordinator

Dr. R. N. Mahalle
Head of Dept

Dr. K. R. Borole
Vice Principal

Dr. A. V. Deshpande
Principal



SINHGAD TECHNICAL EDUCATION SOCIETY'S

SINHGAD INSTITUTE OF TECHNOLOGY (Affiliated

to Savitribai Phule Pune University, Pune & Approved by AICTE)

Gat No. 309/310, off Mumbai Pune Expressway Kusgaon (Bk), Lonavala Pune - 410401

Sinhgad Institutes



Sinhgad Institutes

Sinhgad Technical Education Society's
SMT. KASHIBAI NAVALE COLLEGE OF ENGINEERING,
VADGAON (Bk), Pune -411041

Department of Computer Engineering
TRAIN THE TRAINER (TE)

CERTIFICATE OF PARTICIPATION

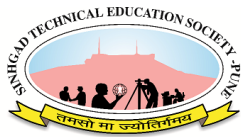
THIS IS TO CERTIFY THAT PROF. S. R. PATIL OF
SIT, Lonavala HAS ATTENDED TRAIN THE TRAINER WORKSHOP FOR
THE SUBJECT TOC ORGANIZED FROM 5TH TO 10TH JUNE 2017.

Prof. V.V. Dakhode
Coordinator

Dr. P.N. Mahalle
Head of Dept

Dr. K.R. Borole
Vice Principal

Dr. A.V. Deshpande
Principal



Sinhgad Institutes

SINHGAD TECHNICAL EDUCATION SOCIETY'S
SINHGAD INSTITUTE OF TECHNOLOGY (Affiliated

to Savitribai Phule Pune University, Pune & Approved by AICTE)

Gat No. 309/310, off Mumbai Pune Expressway Kusgaon (Bk), Lonavala Pune - 410401



Sinhgad Institutes

Sinhgad Technical Education Society's
SMT. KASHIBAI NAVALE COLLEGE OF ENGINEERING,
VADGAON (Bk), Pune -411041

Department of Computer Engineering
TRAIN THE TRAINER (TE)

CERTIFICATE OF PARTICIPATION

THIS IS TO CERTIFY THAT Prof. S. N. LOHAR OF

SIT, Lonavala HAS ATTENDED TRAIN THE TRAINER WORKSHOP FOR

THE SUBJECT SE & PM ORGANIZED FROM 5TH TO 10TH JUNE 2017.

Prof. V.V. Dakhode
Coordinator

Dr. P.N. Mahalle
Head of Dept

Dr. K. R. Borole
Vice Principal

Dr. A.V. Deshpande
Principal



STES's
SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Department of Electrical Engineering

PRESENTS

One week STTP on "Advanced Optimization Techniques with applications in Electrical Engineering"

CERTIFICATE OF PARTICIPATION

This is to certify that **Mr. Kalage Amol Appaso** of **Electrical Engineering, SIT, Lonavala** has participated in One Week STTP on **"Advanced Optimization Techniques with applications in Electrical Engineering"** Organized by Department of Electrical Engineering SIT, Lonavala during Of March 06-10, 2016.

Dr.A.A. Kalage
Conveyer
Electrical Dept. SIT Lonavala

Dr. M. S. Gaikwad
Principal, SIT Lonavala
Campus Director Lonavala



Sardar Vallabhbhai National Institute of Technology (SVNIT), Surat

(An Institute of National Importance of Govt. of India)

Ichchhanath, Surat - 395 007 (Gujarat) India

TEQIP-II Sponsored 16th One Week Short Term Training Program
on

**Advanced Engineering Optimization
Through Intelligent Techniques
(AEOTIT)**

CERTIFICATE

This is to certify that Dr./Mr./Ms.

SARIKA V. TADE

has attended the TEQIP-II Sponsored one week short term training program on

Advanced Engineering Optimization Through Intelligent Techniques (AEOTIT)

held by the Department of Mechanical Engineering, Sardar Vallabhbhai National Institute of Technology,

Surat, India during 22-26 August, 2016.

R. V. Rao

Coordinator

[Signature]

Head, Mech. Engg. Deptt.

[Signature]

Coordinator, TEQIP-II

STES's
SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Department of Electrical Engineering

PRESENTS

One week STTP on "Advanced Optimization Techniques with applications in Electrical Engineering"

CERTIFICATE OF PARTICIPATION

This is to certify that **Mr.Kalgunde Manohar Nivrutti** of **Electrical Engg,SIT,Lonavala** has participated in One Week STTP on **"Advanced Optimization Techniques with applications in Electrical Engineering"** Organized by Department of Electrical Engineering SIT, Lonavala during Of March 06-10, 2016.



Dr.A.A. Kalage
Conveyer
Electrical Dept. SIT Lonavala



Dr. M. S. Gaikwad
Principal, SIT Lonavala
Campus Director Lonavala

STES's
SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Department of Electrical Engineering

PRESENTS

One week STTP on "Advanced Optimization Techniques with applications in Electrical Engineering"

CERTIFICATE OF PARTICIPATION

This is to certify that **Mr. Tamhane Arjun Vitthalrao** of **Electrical Engg, SIT, Lonavala** has participated in One Week STTP on **"Advanced Optimization Techniques with applications in Electrical Engineering"** Organized by Department of Electrical Engineering SIT, Lonavala during Of March 06-10, 2016.



Dr. A. A. Kalage
Conveyer
Electrical Dept. SIT Lonavala



Dr. M. S. Gaikwad
Principal, SIT Lonavala
Campus Director Lonavala



STES's

SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Department of Electrical Engineering

PRESENTS

One week STTP on "Advanced Optimization Techniques with applications in Electrical Engineering"

CERTIFICATE OF PARTICIPATION

This is to certify that **Mr.Mhetre Santosh Laxman** of **Electrical Engg,SIT,Lonavala** has participated in One Week STTP on **"Advanced Optimization Techniques with applications in Electrical Engineering"** Organized by Department of Electrical Engineering SIT, Lonavala during Of March 06-10, 2016.

Dr.A.A. Kalage
Conveyer
Electrical Dept. SIT Lonavala

Dr. M. S. Gaikwad
Principal, SIT Lonavala
Campus Director Lonavala

STES's
SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Department of Electrical Engineering

PRESENTS

One week STTP on "Advanced Optimization Techniques with applications in Electrical Engineering"

CERTIFICATE OF PARTICIPATION

This is to certify that **Mr. Chahyal Dhananjay Madhukar** of **Electrical Engg, SIT, Lonavala** has participated in One Week STTP on **"Advanced Optimization Techniques with applications in Electrical Engineering"** Organized by Department of Electrical Engineering SIT, Lonavala during Of March 06-10, 2016.



Dr. A. A. Kalage
Conveyer
Electrical Dept. SIT Lonavala



Dr. M. S. Gaikwad
Principal, SIT Lonavala
Campus Director Lonavala



STES's

SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Department of Electrical Engineering

PRESENTS

One week STTP on "Advanced Optimization Techniques with applications in Electrical Engineering"

CERTIFICATE OF PARTICIPATION

This is to certify that **Dr. Shenbagalakshmi R.** of **Electrical Engg, SIT, Lonavala** has participated in One Week STTP on **"Advanced Optimization Techniques with applications in Electrical Engineering"** Organized by Department of Electrical Engineering SIT, Lonavala during Of March 06-10, 2016.

Dr.A.A. Kalage
Conveyer
Electrical Dept. SIT Lonavala

Dr. M. S. Gaikwad
Principal, SIT Lonavala
Campus Director Lonavala

STES's
SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Department of Electrical Engineering

PRESENTS

One week STTP on "Advanced Optimization Techniques with applications in Electrical Engineering"

CERTIFICATE OF PARTICIPATION

This is to certify that **Mr.Narkar Rohit Madhukar** of **Electrical Engg,SIT,Lonavala** has participated in One Week STTP on **"Advanced Optimization Techniques with applications in Electrical Engineering"** Organized by Department of Electrical Engineering SIT, Lonavala during Of March 06-10, 2016.



Dr.A.A. Kalage
Conveyer
Electrical Dept. SIT Lonavala



Dr. M. S. Gaikwad
Principal, SIT Lonavala
Campus Director Lonavala

STES's
SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Department of Electrical Engineering

PRESENTS

One week STTP on "Advanced Optimization Techniques with applications in Electrical Engineering"

CERTIFICATE OF PARTICIPATION

This is to certify that **Ms. Sonawane Pradnya Dadasaheb** of **Electrical Engg, SIT, Lonavala** has participated in One Week STTP on **"Advanced Optimization Techniques with applications in Electrical Engineering"** Organized by Department of Electrical Engineering SIT, Lonavala during Of March 06-10, 2016.



Dr.A.A. Kalage
Conveyer
Electrical Dept. SIT Lonavala



Dr. M. S. Gaikwad
Principal, SIT Lonavala
Campus Director Lonavala



STES's

SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Department of Electrical Engineering

PRESENTS

One week STTP on "Advanced Optimization Techniques with applications in Electrical Engineering"

CERTIFICATE OF PARTICIPATION

This is to certify that **Ms. Sonawane Pradnya Dadasaheb** of **Electrical Engg, SIT, Lonavala** has participated in One Week STTP on **"Advanced Optimization Techniques with applications in Electrical Engineering"** Organized by Department of Electrical Engineering SIT, Lonavala during Of March 06-10, 2016.

Dr.A.A. Kalage
Conveyer
Electrical Dept. SIT Lonavala

Dr. M. S. Gaikwad
Principal, SIT Lonavala
Campus Director Lonavala

STES's
SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Department of Electrical Engineering

PRESENTS

One week STTP on "Advanced Optimization Techniques with applications in Electrical Engineering"

CERTIFICATE OF PARTICIPATION

This is to certify that **Mr. Walke Girish Rameshrao** of **Electrical Engg, SIT, Lonavala** has participated in One Week STTP on **"Advanced Optimization Techniques with applications in Electrical Engineering"** Organized by Department of Electrical Engineering SIT, Lonavala during Of March 06-10, 2016.



Dr.A.A. Kalage
Conveyer
Electrical Dept. SIT Lonavala



Dr. M. S. Gaikwad
Principal, SIT Lonavala
Campus Director Lonavala

STES's
SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Department of Electrical Engineering

PRESENTS

One week STTP on "Advanced Optimization Techniques with applications in Electrical Engineering"

CERTIFICATE OF PARTICIPATION

This is to certify that **Ms.Madhuri Uday Naik** of **Electrical Engg, SIT,Lonavala** has participated in One Week STTP on **"Advanced Optimization Techniques with applications in Electrical Engineering"** Organized by Department of Electrical Engineering SIT, Lonavala during Of March 06-10, 2016.



Dr.A.A. Kalage
Conveyer
Electrical Dept. SIT Lonavala



Dr. M. S. Gaikwad
Principal, SIT Lonavala
Campus Director Lonavala

STES's
SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Department of Electronics & Telecommunication Engineering

PRESENTS

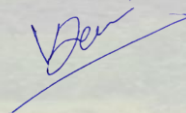
One week FDP on "Foundation of Fuzzy Set and Fuzzy Logic with Applications"

CERTIFICATE OF PARTICIPATION

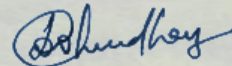
This is to certify that **Mr. Lohar Ganesh Vanji** of **E&Tc dept, SIT, Lonavala** has participated in One Week Faculty Development Program on *"Foundation of Fuzzy Set and Fuzzy Logic with Applications"* Organized by Department of Electronics and Telecommunication Engineering, SIT, Lonavala during October 04-08, 2016.



Prof. R.V. Babar
Co-ordinator
E&TC Dept. SIT Lonavala



Prof. V.V. Deotare
HOD
E&TC Dept. SIT Lonavala



Dr. D. D. Chaudhary
Vice Principal,
SIT Lonavala



Dr. M. S. Gaikwad
Principal, SIT Lonavala

STES's
SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Department of Electronics & Telecommunication Engineering

PRESENTS

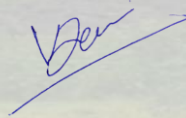
One week FDP on "Foundation of Fuzzy Set and Fuzzy Logic with Applications"

CERTIFICATE OF PARTICIPATION

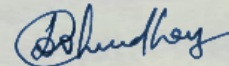
This is to certify that **Ms. More Shital Kiran** of **E&Tc dept, SIT, Lonavala** has participated in One Week Faculty Development Program on **"Foundation of Fuzzy Set and Fuzzy Logic with Applications"** Organized by Department of Electronics and Telecommunication Engineering, SIT, Lonavala during October 04-08, 2016.



Prof. R.V. Babar
Co-ordinator
E&TC Dept. SIT Lonavala



Prof. V.V. Deotare
HOD
E&TC Dept. SIT Lonavala



Dr. D. D. Chaudhary
Vice Principal,
SIT Lonavala



Dr. M. S. Gaikwad
Principal, SIT Lonavala

STES's
SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Department of Electronics & Telecommunication Engineering

PRESENTS

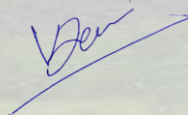
One week FDP on "Foundation of Fuzzy Set and Fuzzy Logic with Applications"

CERTIFICATE OF PARTICIPATION

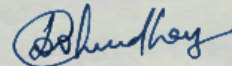
This is to certify that **Mr. Bhosale Manoj Krishna** of **E&Tc dept, SIT, Lonavala** has participated in One Week Faculty Development Program on **"Foundation of Fuzzy Set and Fuzzy Logic with Applications"** Organized by Department of Electronics and Telecommunication Engineering, SIT, Lonavala during October 04-08, 2016.



Prof. R.V. Babar
Co-ordinator
E&TC Dept. SIT Lonavala



Prof. V.V. Deotare
HOD
E&TC Dept. SIT Lonavala



Dr. D. D. Chaudhary
Vice Principal,
SIT Lonavala



Dr. M. S. Gaikwad
Principal, SIT Lonavala

STES's
SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Department of Electronics & Telecommunication Engineering

PRESENTS

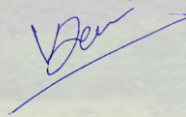
One week FDP on "Foundation of Fuzzy Set and Fuzzy Logic with Applications"

CERTIFICATE OF PARTICIPATION

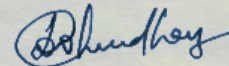
This is to certify that **Mr. Bhad Sharad Arun** of **E&Tc dept, SIT, Lonavala** has participated in One Week Faculty Development Program on *"Foundation of Fuzzy Set and Fuzzy Logic with Applications"* Organized by Department of Electronics and Telecommunication Engineering, SIT, Lonavala during October 04-08, 2016.



Prof. R.V. Babar
Co-ordinator
E&TC Dept. SIT Lonavala



Prof. V.V. Deotare
HOD
E&TC Dept. SIT Lonavala



Dr. D. D. Chaudhary
Vice Principal,
SIT Lonavala



Dr. M. S. Gaikwad
Principal, SIT Lonavala

STES's
SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Department of Electronics & Telecommunication Engineering

PRESENTS

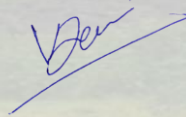
One week FDP on "Foundation of Fuzzy Set and Fuzzy Logic with Applications"

CERTIFICATE OF PARTICIPATION

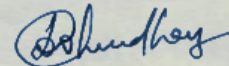
This is to certify that **Ms. Shinde Shilpa Vikas** of **E&Tc dept, SIT, Lonavala** has participated in One Week Faculty Development Program on **"Foundation of Fuzzy Set and Fuzzy Logic with Applications"** Organized by Department of Electronics and Telecommunication Engineering, SIT, Lonavala during October 04-08, 2016.



Prof. R.V. Babar
Co-ordinator
E&TC Dept. SIT Lonavala



Prof. V.V. Deotare
HOD
E&TC Dept. SIT Lonavala



Dr. D. D. Chaudhary
Vice Principal,
SIT Lonavala



Dr. M. S. Gaikwad
Principal, SIT Lonavala

STES's
SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Department of Electronics & Telecommunication Engineering

PRESENTS

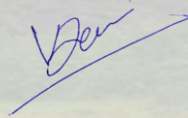
One week FDP on "Foundation of Fuzzy Set and Fuzzy Logic with Applications"

CERTIFICATE OF PARTICIPATION

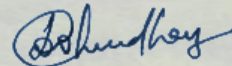
This is to certify that **Mr. Thaksen Jagannath Parvat** of **Engg.Sci. dept. SIT,Lonavala** has participated in One Week Faculty Development Program on **"Foundation of Fuzzy Set and Fuzzy Logic with Applications"** Organized by Department of Electronics and Telecommunication Engineering, SIT, Lonavala during October 04-08, 2016.



Prof. R.V. Babar
Co-ordinator
E&TC Dept. SIT Lonavala



Prof. V.V. Deotare
HOD
E&TC Dept. SIT Lonavala



Dr. D. D. Chaudhary
Vice Principal,
SIT Lonavala



Dr. M. S. Gaikwad
Principal, SIT Lonavala

STES's
SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Department of Electronics & Telecommunication Engineering

PRESENTS

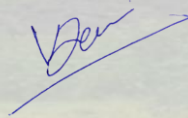
One week FDP on "Foundation of Fuzzy Set and Fuzzy Logic with Applications"

CERTIFICATE OF PARTICIPATION

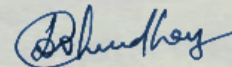
This is to certify that **Ms. Shende Dipali Kedar** of **E&Tc dept, SIT, Lonavala** has participated in One Week Faculty Development Program on **"Foundation of Fuzzy Set and Fuzzy Logic with Applications"** Organized by Department of Electronics and Telecommunication Engineering, SIT, Lonavala during October 04-08, 2016.



Prof. R.V. Babar
Co-ordinator
E&TC Dept. SIT Lonavala



Prof. V.V. Deotare
HOD
E&TC Dept. SIT Lonavala



Dr. D. D. Chaudhary
Vice Principal,
SIT Lonavala



Dr. M. S. Gaikwad
Principal, SIT Lonavala

STES's
SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Department of Electronics & Telecommunication Engineering

PRESENTS

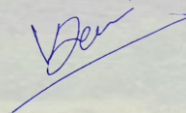
One week FDP on "Foundation of Fuzzy Set and Fuzzy Logic with Applications"

CERTIFICATE OF PARTICIPATION

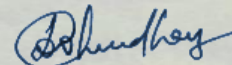
This is to certify that **Mr. Labade Anand Arjun** of **E&Tc dept, SIT, Lonavala** has participated in One Week Faculty Development Program on **"Foundation of Fuzzy Set and Fuzzy Logic with Applications"** Organized by Department of Electronics and Telecommunication Engineering, SIT, Lonavala during October 04-08, 2016.



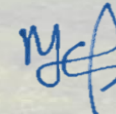
Prof. R.V. Babar
Co-ordinator
E&TC Dept. SIT Lonavala



Prof. V.V. Deotare
HOD
E&TC Dept. SIT Lonavala



Dr. D. D. Chaudhary
Vice Principal,
SIT Lonavala



Dr. M. S. Gaikwad
Principal, SIT Lonavala

STES's
SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Department of Electronics & Telecommunication Engineering

PRESENTS

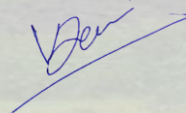
One week FDP on "Foundation of Fuzzy Set and Fuzzy Logic with Applications"

CERTIFICATE OF PARTICIPATION

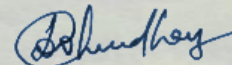
This is to certify that **Dr. Singh Dhananjay Kumar** of **E&Tc dept, SIT, Lonavala** has participated in One Week Faculty Development Program on **"Foundation of Fuzzy Set and Fuzzy Logic with Applications"** Organized by Department of Electronics and Telecommunication Engineering, SIT, Lonavala during October 04-08, 2016.



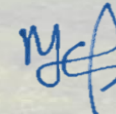
Prof. R.V. Babar
Co-ordinator
E&TC Dept. SIT Lonavala



Prof. V.V. Deotare
HOD
E&TC Dept. SIT Lonavala



Dr. D. D. Chaudhary
Vice Principal,
SIT Lonavala



Dr. M. S. Gaikwad
Principal, SIT Lonavala

STES's
SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Department of Electronics & Telecommunication Engineering

PRESENTS

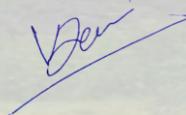
One week FDP on "Foundation of Fuzzy Set and Fuzzy Logic with Applications"

CERTIFICATE OF PARTICIPATION

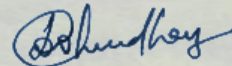
This is to certify that **Mr. Deotare Vilas Vasudeo** of **E&Tc dept, SIT, Lonavala** has participated in One Week Faculty Development Program on **"Foundation of Fuzzy Set and Fuzzy Logic with Applications"** Organized by Department of Electronics and Telecommunication Engineering, SIT, Lonavala during October 04-08, 2016.



Prof. R.V. Babar
Co-ordinator
E&TC Dept. SIT Lonavala



Prof. V.V. Deotare
HOD
E&TC Dept. SIT Lonavala



Dr. D. D. Chaudhary
Vice Principal,
SIT Lonavala



Dr. M. S. Gaikwad
Principal, SIT Lonavala

STES's
SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Department of Electronics & Telecommunication Engineering

PRESENTS

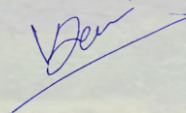
One week FDP on "Foundation of Fuzzy Set and Fuzzy Logic with Applications"

CERTIFICATE OF PARTICIPATION

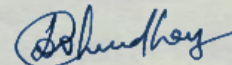
This is to certify that **Mr. Mantri Dnyaneshwar Shriranglal** of **E&Tc dept, SIT, Lonavala** has participated in One Week Faculty Development Program on *"Foundation of Fuzzy Set and Fuzzy Logic with Applications"* Organized by Department of Electronics and Telecommunication Engineering, SIT, Lonavala during October 04-08, 2016.



Prof. R.V. Babar
Co-ordinator
E&TC Dept. SIT Lonavala



Prof. V.V. Deotare
HOD
E&TC Dept. SIT Lonavala



Dr. D. D. Chaudhary
Vice Principal,
SIT Lonavala



Dr. M. S. Gaikwad
Principal, SIT Lonavala

STES's
SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Department of Electronics & Telecommunication Engineering

PRESENTS

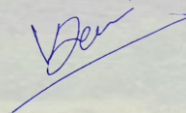
One week FDP on "Foundation of Fuzzy Set and Fuzzy Logic with Applications"

CERTIFICATE OF PARTICIPATION

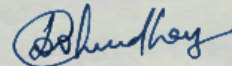
This is to certify that **Mr. Gholap Sharad Balasaheb** of **E&Tc dept, SIT, Lonavala** has participated in One Week Faculty Development Program on **"Foundation of Fuzzy Set and Fuzzy Logic with Applications"** Organized by Department of Electronics and Telecommunication Engineering, SIT, Lonavala during October 04-08, 2016.



Prof. R.V. Babar
Co-ordinator
E&TC Dept. SIT Lonavala



Prof. V.V. Deotare
HOD
E&TC Dept. SIT Lonavala



Dr. D. D. Chaudhary
Vice Principal,
SIT Lonavala



Dr. M. S. Gaikwad
Principal, SIT Lonavala

STES's
SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Department of Electronics & Telecommunication Engineering

PRESENTS

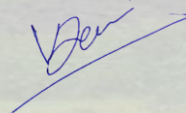
One week FDP on "Foundation of Fuzzy Set and Fuzzy Logic with Applications"

CERTIFICATE OF PARTICIPATION

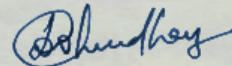
This is to certify that **Ms. Patil Sulakshana Sudhakar** of **E&Tc dept, SIT, Lonavala** has participated in One Week Faculty Development Program on **"Foundation of Fuzzy Set and Fuzzy Logic with Applications"** Organized by Department of Electronics and Telecommunication Engineering, SIT, Lonavala during October 04-08, 2016.



Prof. R.V. Babar
Co-ordinator
E&TC Dept. SIT Lonavala



Prof. V.V. Deotare
HOD
E&TC Dept. SIT Lonavala



Dr. D. D. Chaudhary
Vice Principal,
SIT Lonavala



Dr. M. S. Gaikwad
Principal, SIT Lonavala

STES's
SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Department of Electronics & Telecommunication Engineering

PRESENTS

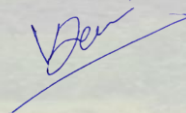
One week FDP on "Foundation of Fuzzy Set and Fuzzy Logic with Applications"

CERTIFICATE OF PARTICIPATION

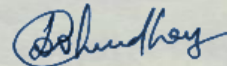
This is to certify that **Ms. Gangane Jyoti Ramesh** of **E&Tc dept, SIT, Lonavala** has participated in One Week Faculty Development Program on **"Foundation of Fuzzy Set and Fuzzy Logic with Applications"** Organized by Department of Electronics and Telecommunication Engineering, SIT, Lonavala during October 04-08, 2016.



Prof. R.V. Babar
Co-ordinator
E&TC Dept. SIT Lonavala



Prof. V.V. Deotare
HOD
E&TC Dept. SIT Lonavala



Dr. D. D. Chaudhary
Vice Principal,
SIT Lonavala



Dr. M. S. Gaikwad
Principal, SIT Lonavala

STES's
SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Department of Electronics & Telecommunication Engineering

PRESENTS

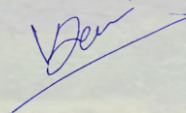
One week FDP on "Foundation of Fuzzy Set and Fuzzy Logic with Applications"

CERTIFICATE OF PARTICIPATION

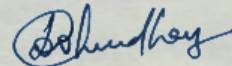
This is to certify that **Mr. Dike Prashant Rajaramji** of **E&Tc dept, SIT, Lonavala** has participated in One Week Faculty Development Program on **"Foundation of Fuzzy Set and Fuzzy Logic with Applications"** Organized by Department of Electronics and Telecommunication Engineering, SIT, Lonavala during October 04-08, 2016.



Prof. R.V. Babar
Co-ordinator
E&TC Dept. SIT Lonavala



Prof. V.V. Deotare
HOD
E&TC Dept. SIT Lonavala



Dr. D. D. Chaudhary
Vice Principal,
SIT Lonavala



Dr. M. S. Gaikwad
Principal, SIT Lonavala

STES's
SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Department of Electronics & Telecommunication Engineering

PRESENTS

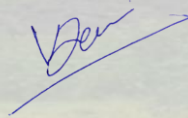
One week FDP on "Foundation of Fuzzy Set and Fuzzy Logic with Applications"

CERTIFICATE OF PARTICIPATION

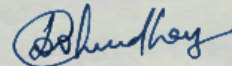
This is to certify that **Mr. Babar Rajendra Vithal** of **E&Tc dept, SIT, Lonavala** has participated in One Week Faculty Development Program on *"Foundation of Fuzzy Set and Fuzzy Logic with Applications"* Organized by Department of Electronics and Telecommunication Engineering, SIT, Lonavala during October 04-08, 2016.



Prof. R.V. Babar
Co-ordinator
E&TC Dept. SIT Lonavala



Prof. V.V. Deotare
HOD
E&TC Dept. SIT Lonavala



Dr. D. D. Chaudhary
Vice Principal,
SIT Lonavala



Dr. M. S. Gaikwad
Principal, SIT Lonavala

STES's
SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Department of Electronics & Telecommunication Engineering

PRESENTS

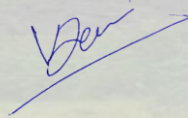
One week FDP on "Foundation of Fuzzy Set and Fuzzy Logic with Applications"

CERTIFICATE OF PARTICIPATION

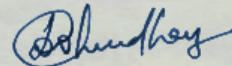
This is to certify that **Ms. Kamble Pradnya Avinash** of **E&Tc dept, SIT, Lonavala** has participated in One Week Faculty Development Program on **"Foundation of Fuzzy Set and Fuzzy Logic with Applications"** Organized by Department of Electronics and Telecommunication Engineering, SIT, Lonavala during October 04-08, 2016.



Prof. R.V. Babar
Co-ordinator
E&TC Dept. SIT Lonavala



Prof. V.V. Deotare
HOD
E&TC Dept. SIT Lonavala



Dr. D. D. Chaudhary
Vice Principal,
SIT Lonavala



Dr. M. S. Gaikwad
Principal, SIT Lonavala

STES's
SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Department of Electronics & Telecommunication Engineering

PRESENTS

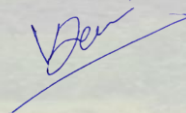
One week FDP on "Foundation of Fuzzy Set and Fuzzy Logic with Applications"

CERTIFICATE OF PARTICIPATION

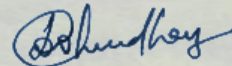
This is to certify that **Ms. Bhardwaj Sanika Sagar** of **E&Tc dept, SIT, Lonavala** has participated in One Week Faculty Development Program on **"Foundation of Fuzzy Set and Fuzzy Logic with Applications"** Organized by Department of Electronics and Telecommunication Engineering, SIT, Lonavala during October 04-08, 2016.



Prof. R.V. Babar
Co-ordinator
E&TC Dept. SIT Lonavala



Prof. V.V. Deotare
HOD
E&TC Dept. SIT Lonavala



Dr. D. D. Chaudhary
Vice Principal,
SIT Lonavala



Dr. M. S. Gaikwad
Principal, SIT Lonavala

STES's
SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Department of Electronics & Telecommunication Engineering

PRESENTS

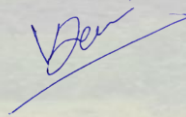
One week FDP on "Foundation of Fuzzy Set and Fuzzy Logic with Applications"

CERTIFICATE OF PARTICIPATION

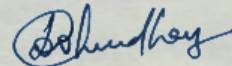
This is to certify that **Ms. Tayde Bhavana Mohan** of **Engg. Sci. dept. SIT, Lonavala** has participated in One Week Faculty Development Program on **"Foundation of Fuzzy Set and Fuzzy Logic with Applications"** Organized by Department of Electronics and Telecommunication Engineering, SIT, Lonavala during October 04-08, 2016.



Prof. R.V. Babar
Co-ordinator
E&TC Dept. SIT Lonavala



Prof. V.V. Deotare
HOD
E&TC Dept. SIT Lonavala



Dr. D. D. Chaudhary
Vice Principal,
SIT Lonavala



Dr. M. S. Gaikwad
Principal, SIT Lonavala

STES's
SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Department of Electronics & Telecommunication Engineering

PRESENTS

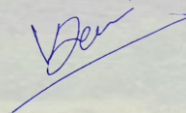
One week FDP on "Foundation of Fuzzy Set and Fuzzy Logic with Applications"

CERTIFICATE OF PARTICIPATION

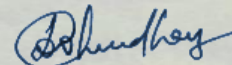
This is to certify that **Ms. Chakranarayan Priyanka Ravindra** of **Engg. Sci. dept. SIT, Lonavala** has participated in One Week Faculty Development Program on **"Foundation of Fuzzy Set and Fuzzy Logic with Applications"** Organized by Department of Electronics and Telecommunication Engineering, SIT, Lonavala during October 04-08, 2016.



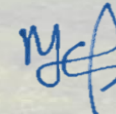
Prof. R.V. Babar
Co-ordinator
E&TC Dept. SIT Lonavala



Prof. V.V. Deotare
HOD
E&TC Dept. SIT Lonavala



Dr. D. D. Chaudhary
Vice Principal,
SIT Lonavala



Dr. M. S. Gaikwad
Principal, SIT Lonavala

STES's
SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Department of Electronics & Telecommunication Engineering

PRESENTS

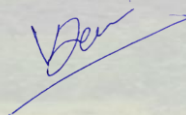
One week FDP on "Foundation of Fuzzy Set and Fuzzy Logic with Applications"

CERTIFICATE OF PARTICIPATION

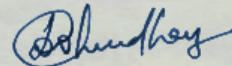
This is to certify that **Ms. Chavan Ashwini Nitin** of **Engg. Sci. dept. SIT, Lonavala** has participated in One Week Faculty Development Program on **"Foundation of Fuzzy Set and Fuzzy Logic with Applications"** Organized by Department of Electronics and Telecommunication Engineering, SIT, Lonavala during October 04-08, 2016.



Prof. R.V. Babar
Co-ordinator
E&TC Dept. SIT Lonavala



Prof. V.V. Deotare
HOD
E&TC Dept. SIT Lonavala



Dr. D. D. Chaudhary
Vice Principal,
SIT Lonavala



Dr. M. S. Gaikwad
Principal, SIT Lonavala

STES's
SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Department of Electronics & Telecommunication Engineering

PRESENTS

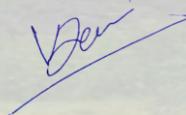
One week FDP on "Foundation of Fuzzy Set and Fuzzy Logic with Applications"

CERTIFICATE OF PARTICIPATION

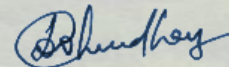
This is to certify that **Mr. Waghmare Mangesh Ananta** of **Engg. Sci. dept. SIT, Lonavala** has participated in One Week Faculty Development Program on **"Foundation of Fuzzy Set and Fuzzy Logic with Applications"** Organized by Department of Electronics and Telecommunication Engineering, SIT, Lonavala during October 04-08, 2016.



Prof. R.V. Babar
Co-ordinator
E&TC Dept. SIT Lonavala



Prof. V.V. Deotare
HOD
E&TC Dept. SIT Lonavala



Dr. D. D. Chaudhary
Vice Principal,
SIT Lonavala



Dr. M. S. Gaikwad
Principal, SIT Lonavala

STES's
SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Department of Electronics & Telecommunication Engineering

PRESENTS

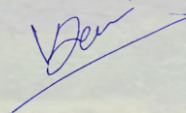
One week FDP on "Foundation of Fuzzy Set and Fuzzy Logic with Applications"

CERTIFICATE OF PARTICIPATION

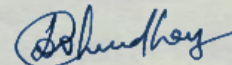
This is to certify that **Dr. Bhosekar Gaurav Vikram** of **Engg. Sci. dept. SIT, Lonavala** has participated in One Week Faculty Development Program on *"Foundation of Fuzzy Set and Fuzzy Logic with Applications"* Organized by Department of Electronics and Telecommunication Engineering, SIT, Lonavala during October 04-08, 2016.



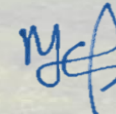
Prof. R.V. Babar
Co-ordinator
E&TC Dept. SIT Lonavala



Prof. V.V. Deotare
HOD
E&TC Dept. SIT Lonavala



Dr. D. D. Chaudhary
Vice Principal,
SIT Lonavala



Dr. M. S. Gaikwad
Principal, SIT Lonavala

STES's
SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Department of Electronics & Telecommunication Engineering

PRESENTS

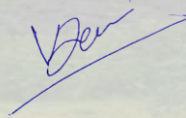
One week FDP on "Foundation of Fuzzy Set and Fuzzy Logic with Applications"

CERTIFICATE OF PARTICIPATION

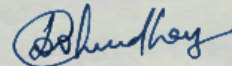
This is to certify that **Mr. Chipade Amar Mahadeo** of **Engg. Sci. dept. SIT, Lonavala** has participated in One Week Faculty Development Program on *"Foundation of Fuzzy Set and Fuzzy Logic with Applications"* Organized by Department of Electronics and Telecommunication Engineering, SIT, Lonavala during October 04-08, 2016.



Prof. R.V. Babar
Co-ordinator
E&TC Dept. SIT Lonavala



Prof. V.V. Deotare
HOD
E&TC Dept. SIT Lonavala



Dr. D. D. Chaudhary
Vice Principal,
SIT Lonavala



Dr. M. S. Gaikwad
Principal, SIT Lonavala

STES's
SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Department of Electronics & Telecommunication Engineering

PRESENTS

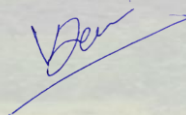
One week FDP on "Foundation of Fuzzy Set and Fuzzy Logic with Applications"

CERTIFICATE OF PARTICIPATION

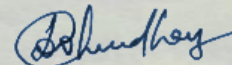
This is to certify that **Ms. Deshmukh Anagha Shantanu** of **Engg. Sci. dept. SIT, Lonavala** has participated in One Week Faculty Development Program on **"Foundation of Fuzzy Set and Fuzzy Logic with Applications"** Organized by Department of Electronics and Telecommunication Engineering, SIT, Lonavala during October 04-08, 2016.



Prof. R.V. Babar
Co-ordinator
E&TC Dept. SIT Lonavala



Prof. V.V. Deotare
HOD
E&TC Dept. SIT Lonavala



Dr. D. D. Chaudhary
Vice Principal,
SIT Lonavala



Dr. M. S. Gaikwad
Principal, SIT Lonavala

STES's
SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Department of Electronics & Telecommunication Engineering

PRESENTS

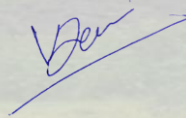
One week FDP on "Foundation of Fuzzy Set and Fuzzy Logic with Applications"

CERTIFICATE OF PARTICIPATION

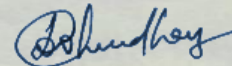
This is to certify that **Ms. Patil Aishwarya Raghunath** of **Engg. Sci. dept. SIT, Lonavala** has participated in One Week Faculty Development Program on *"Foundation of Fuzzy Set and Fuzzy Logic with Applications"* Organized by Department of Electronics and Telecommunication Engineering, SIT, Lonavala during October 04-08, 2016.



Prof. R.V. Babar
Co-ordinator
E&TC Dept. SIT Lonavala



Prof. V.V. Deotare
HOD
E&TC Dept. SIT Lonavala



Dr. D. D. Chaudhary
Vice Principal,
SIT Lonavala



Dr. M. S. Gaikwad
Principal, SIT Lonavala

STES's
SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Department of Electronics & Telecommunication Engineering

PRESENTS

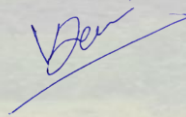
One week FDP on "Foundation of Fuzzy Set and Fuzzy Logic with Applications"

CERTIFICATE OF PARTICIPATION

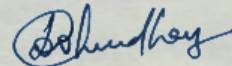
This is to certify that **Mr. Borse Yashwant Jayprakash** of **Engg. Sci. dept. SIT, Lonavala** has participated in One Week Faculty Development Program on **"Foundation of Fuzzy Set and Fuzzy Logic with Applications"** Organized by Department of Electronics and Telecommunication Engineering, SIT, Lonavala during October 04-08, 2016.



Prof. R.V. Babar
Co-ordinator
E&TC Dept. SIT Lonavala



Prof. V.V. Deotare
HOD
E&TC Dept. SIT Lonavala



Dr. D. D. Chaudhary
Vice Principal,
SIT Lonavala



Dr. M. S. Gaikwad
Principal, SIT Lonavala

STES's
SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Department of Electronics & Telecommunication Engineering

PRESENTS

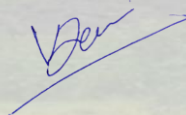
One week FDP on "Foundation of Fuzzy Set and Fuzzy Logic with Applications"

CERTIFICATE OF PARTICIPATION

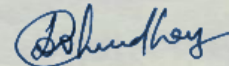
This is to certify that **Mr. Shete Nikhil Anil** of **Engg. Sci. dept. SIT, Lonavala** has participated in One Week Faculty Development Program on *"Foundation of Fuzzy Set and Fuzzy Logic with Applications"* Organized by Department of Electronics and Telecommunication Engineering, SIT, Lonavala during October 04-08, 2016.



Prof. R.V. Babar
Co-ordinator
E&TC Dept. SIT Lonavala



Prof. V.V. Deotare
HOD
E&TC Dept. SIT Lonavala



Dr. D. D. Chaudhary
Vice Principal,
SIT Lonavala



Dr. M. S. Gaikwad
Principal, SIT Lonavala

STES's
SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Department of Electronics & Telecommunication Engineering

PRESENTS

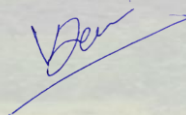
One week FDP on "Foundation of Fuzzy Set and Fuzzy Logic with Applications"

CERTIFICATE OF PARTICIPATION

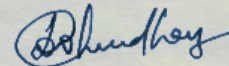
This is to certify that **Mr. Pitake Satish Avaba** of **Engg. Sci. dept. SIT, Lonavala** has participated in One Week Faculty Development Program on **"Foundation of Fuzzy Set and Fuzzy Logic with Applications"** Organized by Department of Electronics and Telecommunication Engineering, SIT, Lonavala during October 04-08, 2016.



Prof. R.V. Babar
Co-ordinator
E&TC Dept. SIT Lonavala



Prof. V.V. Deotare
HOD
E&TC Dept. SIT Lonavala



Dr. D. D. Chaudhary
Vice Principal,
SIT Lonavala



Dr. M. S. Gaikwad
Principal, SIT Lonavala

STES's
SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Department of Electronics & Telecommunication Engineering

PRESENTS

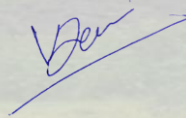
One week FDP on "Foundation of Fuzzy Set and Fuzzy Logic with Applications"

CERTIFICATE OF PARTICIPATION

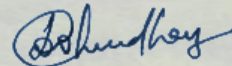
This is to certify that **Mr. Kulkarni Paresh Bhaskar** of **Engg. Sci. dept. SIT, Lonavala** has participated in One Week Faculty Development Program on **"Foundation of Fuzzy Set and Fuzzy Logic with Applications"** Organized by Department of Electronics and Telecommunication Engineering, SIT, Lonavala during October 04-08, 2016.



Prof. R.V. Babar
Co-ordinator
E&TC Dept. SIT Lonavala



Prof. V.V. Deotare
HOD
E&TC Dept. SIT Lonavala



Dr. D. D. Chaudhary
Vice Principal,
SIT Lonavala



Dr. M. S. Gaikwad
Principal, SIT Lonavala

STES's
SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Department of Electronics & Telecommunication Engineering

PRESENTS

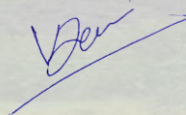
One week FDP on "Foundation of Fuzzy Set and Fuzzy Logic with Applications"

CERTIFICATE OF PARTICIPATION

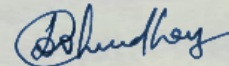
This is to certify that **Mr. Udawant Amol Pandharinath** of **Engg. Sci. dept. SIT, Lonavala** has participated in One Week Faculty Development Program on *"Foundation of Fuzzy Set and Fuzzy Logic with Applications"* Organized by Department of Electronics and Telecommunication Engineering, SIT, Lonavala during October 04-08, 2016.



Prof. R.V. Babar
Co-ordinator
E&TC Dept. SIT Lonavala



Prof. V.V. Deotare
HOD
E&TC Dept. SIT Lonavala



Dr. D. D. Chaudhary
Vice Principal,
SIT Lonavala



Dr. M. S. Gaikwad
Principal, SIT Lonavala

STES's
SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Department of Electronics & Telecommunication Engineering

PRESENTS

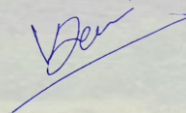
One week FDP on "Foundation of Fuzzy Set and Fuzzy Logic with Applications"

CERTIFICATE OF PARTICIPATION

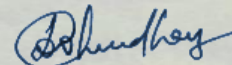
This is to certify that **Mr. Borate Shivling Ramling** of **Engg. Sci. dept. SIT, Lonavala** has participated in One Week Faculty Development Program on **"Foundation of Fuzzy Set and Fuzzy Logic with Applications"** Organized by Department of Electronics and Telecommunication Engineering, SIT, Lonavala during October 04-08, 2016.



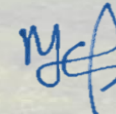
Prof. R.V. Babar
Co-ordinator
E&TC Dept. SIT Lonavala



Prof. V.V. Deotare
HOD
E&TC Dept. SIT Lonavala



Dr. D. D. Chaudhary
Vice Principal,
SIT Lonavala



Dr. M. S. Gaikwad
Principal, SIT Lonavala

STES's
SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Department of Electronics & Telecommunication Engineering

PRESENTS

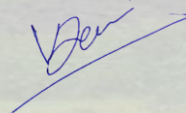
One week FDP on "Foundation of Fuzzy Set and Fuzzy Logic with Applications"

CERTIFICATE OF PARTICIPATION

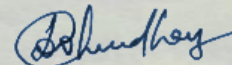
This is to certify that **Ms. Mulye Shruti Shyamduatta** of **Engg. Sci. dept. SIT, Lonavala** has participated in One Week Faculty Development Program on **"Foundation of Fuzzy Set and Fuzzy Logic with Applications"** Organized by Department of Electronics and Telecommunication Engineering, SIT, Lonavala during October 04-08, 2016.



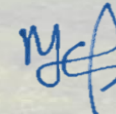
Prof. R.V. Babar
Co-ordinator
E&TC Dept. SIT Lonavala



Prof. V.V. Deotare
HOD
E&TC Dept. SIT Lonavala



Dr. D. D. Chaudhary
Vice Principal,
SIT Lonavala



Dr. M. S. Gaikwad
Principal, SIT Lonavala

STES's
SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Department of Electronics & Telecommunication Engineering

PRESENTS

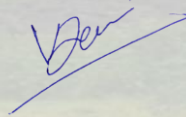
One week FDP on "Foundation of Fuzzy Set and Fuzzy Logic with Applications"

CERTIFICATE OF PARTICIPATION

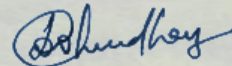
This is to certify that **Mr. Patil Pravin Gopinath** of **Engg. Sci. dept. SIT, Lonavala** has participated in One Week Faculty Development Program on *"Foundation of Fuzzy Set and Fuzzy Logic with Applications"* Organized by Department of Electronics and Telecommunication Engineering, SIT, Lonavala during October 04-08, 2016.



Prof. R.V. Babar
Co-ordinator
E&TC Dept. SIT Lonavala



Prof. V.V. Deotare
HOD
E&TC Dept. SIT Lonavala



Dr. D. D. Chaudhary
Vice Principal,
SIT Lonavala



Dr. M. S. Gaikwad
Principal, SIT Lonavala