



SINHGAD TECHNICAL EDUCATION SOCIETY'S SINHGAD INSTITUTE OF TECHNOLOGY

Sinhgad Institutes

(Affiliated to Savitribai Phule Pune University, Pune & Approved by AICTE)

Gat No. 309/310, off Mumbai Pune Expressway Kusgaon (Bk), Lonavala Pune – 410401

Website: sit.sinhgad.edu

Academic Year-2016-17

Sr. No	Name of teacher participated	Subject	Department	Name of the body in which full time teacher participated	Date of Nomination
1	Dr. Babar sachin Dilip	Advanced Data Structures	Computer Engineering	Setting of question papers for UG/PG programs	28/02/2017
2	Ms. Ahire Pallavi Pankaj	Computer graphics	Information Technology	Setting of question papers for UG programs	20/08/2016
3	Dr. Chaudhary Dilip Damodar	Television & video engg.	E & TC Engineering	Setting of question papers for UG programs	16/03/2017
4	Dr. Mantri Dnyaneshwar Shriranglal	Micro Controller & Applications	E & TC Engineering	Setting of question papers for UG programs	25/08/2016
5	Dr. Gholap Sharad Balasaheb	Audio Video Engg	E & TC Engineering	Setting of question papers for UG programs	17/08/2016
6	Prof.Dike Prashant Rajaramji	Broadband Comm.	E & TC Engineering	Setting of question papers for UG programs	17/08/2016
7	Dr. Babar sachin Dilip	Data structures & files	Computer Engineering	Design and Development of Curriculum	16/03/2017
8	Ms. Ahire Pallavi Pankaj	Object oriented programming	Information Technology	Design and Development of Curriculum	01/06/2016
9	Dr. Babar sachin Dilip	Advanced data structures	Computer Engineering	Assessment /evaluation process	Oct/Nov2016
10	Mr.Chaudhari Manohar Santosh	microprocessor	Computer Engineering	Assessment /evaluation process	Oct/Nov2016
11	Ms.Patil Shubhangi Raju	Computer graphics and gaming	Computer Engineering	Assessment /evaluation process	Oct/Nov2016
12	Mr. Nibekar Shrikrishana Bhanudas	Computer organization	Computer Engineering	Assessment /evaluation process	Oct/Nov2016
13	Ms.Lamgunde Anuradha Bajarang	Discrete mathematics	Computer Engineering	Assessment /evaluation process	Oct/Nov2016
14	Mr Nalavade Jagannath Eknath	Digital electronics and logic design	Computer Engineering	Assessment /evaluation process	Oct/Nov2016
15	Ms.Chavan Jai Pramod	Data structures and algorithm	Computer Engineering	Assessment /evaluation process	Oct/Nov2016
16	Mr. Patil Nagesh Keshvrao	Digital electronics and logic design	Computer Engineering	Assessment /evaluation process	Oct/Nov2016
17	Mr. Nalawade shekhar	Digital electronics and logic design	Computer Engineering	Assessment /evaluation process	Oct/Nov2016
18	Mr.Bandal Amol	Object oriented	Computer	Assessment /evaluation process	Oct/Nov2016

ATTESTED

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(Dr. M. S. Gaikwad)
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	Nivrutti	programming	Engineering	process	
19	Mr. Shah Kunal Ravindra	Computer graphics and gaming	Computer Engineering	Assessment /evaluation process	Oct/Nov2016
20	Mr.Wandre Sachin Narayan	Data structures and algorithm	Computer Engineering	Assessment /evaluation process	Oct/Nov2016
21	Mr.Shedole Sambhaje Marutirao	Principles of prog. Language.	Computer Engineering	Assessment /evaluation process	Oct/Nov2016
22	Mr Sagare Abhijeet Vitthal	Computer organization	Computer Engineering	Assessment /evaluation process	Oct/Nov2016
23	Mr.Shaikh Salim Gulab	Principles of prog. Language	Computer Engineering	Assessment /evaluation process	Oct/Nov2016
24	Ms. Adikane Tejaswini Vitthalrao	Computer organization	Computer Engineering	Assessment /evaluation process	Oct/Nov2016
25	Mr.Raut Prahant Vyankatrao	microprocessor	Computer Engineering	Assessment /evaluation process	Oct/Nov2016
26	Ms. Lohar shailaja Nitin	Object oriented programming	Computer Engineering	Assessment /evaluation process	Oct/Nov2016
27	Ms.Kumbhar Madhuri Narayan	Principles of prog.language	Computer Engineering	Assessment /evaluation process	Oct/Nov2016
28	Mr.Dongare Prashant Bajrang	Object oriented programming	Computer Engineering	Assessment /evaluation process	Oct/Nov2016
29	Mr. .Gholap Sharad Balasaheb	Analog Communication	E & TC Engineering	Assessment /evaluation process	Oct/Nov2016
30	Ms. Patil Sulakshana Sudhakar	Analog Communication	E & TC Engineering	Assessment /evaluation process	Oct/Nov2016
31	Mr. Dike Prashant Rajaramji	Signals & Systems	E & TC Engineering	Assessment /evaluation process	Oct/Nov2016
32	Ms. Kamble Pradnya Avinash	Signals & Systems	E & TC Engineering	Assessment /evaluation process	Oct/Nov2016
33	Ms. Bhardwaj Sanika Sagar	Electronic Devices & Circuits	E & TC Engineering	Assessment /evaluation process	May/June 2017
34	Ms. Shinde Shilpa Vikas	Signals & Systems	E & TC Engineering	Assessment /evaluation process	May/June 2017
35	Ms. Shende Dipali Kedar	Analog Communication	E & TC Engineering	Assessment /evaluation process	May/June 2017
36	Mr. Labade Anand Arjun	Data structures and algorithm	E & TC Engineering	Assessment /evaluation process	May/June 2017
37	Ms. Rajeshwarkar Vijaya Gopalrao	Integrated Circuits	E & TC Engineering	Assessment /evaluation process	May/June 2017
38	Mr. Bhosale Manoj Krishna	Electronic Devices & Circuits	E & TC Engineering	Assessment /evaluation process	May/June 2017
39	Mr. Bhad Sharad Arun	Electronic Devices & Circuits	E & TC Engineering	Assessment /evaluation process	May/June 2017

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40	Mr. Chavan Vikram Madhukar	Digital electronics	E & TC Engineering	Assessment /evaluation process	May/June 2017
41	Ms. Velhal Vaishali Vilas	Object Oriented Programming	E & TC Engineering	Assessment /evaluation process	May/June 2017
42	Mr. Pachpor Nishant Nilkanth	Object Oriented Programming	E & TC Engineering	Assessment /evaluation process	May/June 2017
43	Mr. Shinde Suhas Deelip	Analog Communication	E & TC Engineering	Assessment /evaluation process	May/June 2017
44	Ms. Shitole Nilima Bhausaheb	Digital electronics	E & TC Engineering	Assessment /evaluation process	May/June 2017
45	Mr. Bhalerao Gajanan Anandrao	Integrated Circuits	E & TC Engineering	Assessment /evaluation process	May/June 2017
46	Mr. Nalawade Nilesh Subhash	Control Systems	E & TC Engineering	Assessment /evaluation process	May/June 2017
47	Mr. Nagime Amol Pandharinath	Control Systems	E & TC Engineering	Assessment /evaluation process	May/June 2017
48	Mr. Narkar Rohit Madhukar	Electronics and Electrical Engg.	Electrical Engineering	Assessment /evaluation process	May/June 2017
49	Dr. Shinde. Vikas. Vitthal	Applied Thermodynamics	Mechanical Engineering	Assessment /evaluation process	May/June 2017
50	Mr. Gaikwad Sambhaji Mahadeo	Manufacturing Process-I	Mechanical Engineering	Assessment /evaluation process	May/June 2017
51	Mr. Puranik Laxmikant Purushottam	Fluid Mechanics	Mechanical Engineering	Assessment /evaluation process	May/June 2017
52	Mr. Hirulkar Nitesh Hirulkar	Material Science	Mechanical Engineering	Assessment /evaluation process	May/June 2017
53	Ms. Saddu Smita Channavir	Manufacturing Process-I	Mechanical Engineering	Assessment /evaluation process	May/June 2017
54	Mr. Patil Sameer Ganeshrao	Applied Thermodynamics	Mechanical Engineering	Assessment /evaluation process	May/June 2017
55	Mr. Lokhande Prasad Eknath	Material Science	Mechanical Engineering	Assessment /evaluation process	May/June 2017
56	Ms. Jawale Pratiksha Prakash	Theory of Machines – I	Mechanical Engineering	Assessment /evaluation process	May/June 2017
57	Mr. Wasnik Amit Ramesh	Theory of Machines – I	Mechanical Engineering	Assessment /evaluation process	May/June 2017
58	Mr. Narode Annasaheb Ramdas	Fluid Mechanics	Mechanical Engineering	Assessment /evaluation process	May/June 2017
59	Mr. Ugare Vivek Malgonda	Fluid Mechanics	Mechanical Engineering	Assessment /evaluation process	May/June 2017
60	Mr. Karankoti Shrikanth .Vithoba	Applied Thermodynamics	Mechanical Engineering	Assessment /evaluation process	May/June 2017
61	Mr. Patil Ravichandra Somshekhara	Applied Thermodynamics	Mechanical Engineering	Assessment /evaluation process	May/June 2017
62	Mr. Pesode Pralhad Atmaram	Manufacturing Process-I	Mechanical Engineering	Assessment /evaluation process	May/June 2017

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63	Mr. Mulye Shailesh Mukund	Mathematics-III	Mechanical Engineering	Assessment /evaluation process	May/June 2017
64	Mr. Ogale Agnivesh Purushottam	Fluid Mechanics	Mechanical Engineering	Assessment /evaluation process	May/June 2017
65	Mr. Mehtre Datta Nagorao	Fluid Mechanics	Mechanical Engineering	Assessment /evaluation process	May/June 2017
66	Mr. Neharkar Suresh Prakash	Strength of Materials	Mechanical Engineering	Assessment /evaluation process	May/June 2017
67	Mr. Bharadiya Suraj Ishwarlal	Electronics and Electrical Engg.	Mechanical Engineering	Assessment /evaluation process	May/June 2017
68	Mr. Datar Pranav Prakash	Strength of Materials	Mechanical Engineering	Assessment /evaluation process	May/June 2017
69	Mr. Dube Chinmay Chandrakant	Applied Thermodynamics	Mechanical Engineering	Assessment /evaluation process	May/June 2017
70	Mr. Satpute Anandkumar Arun	Strength of Materials	Mechanical Engineering	Assessment /evaluation process	May/June 2017
71	Mr. Nibekar Shrikrishana Bhanudas	Microprocessor architecture	Computer Engineering	Assessment /evaluation process	25/10/2016
72	Ms.Dhote Bharati Laxmanrao	Dissertation stage-I	Computer Engineering	Assessment /evaluation process	12/1/2017
73	Ms. Tonde Vandana Prakash	Machine learning	Computer Engineering	Assessment /evaluation process	28/11/2016
74	Ms. Kulkarni Anuradha Prasad	Processor architecture interfacing	Computer Engineering	Assessment /evaluation process	29/05/2017
75	Mr.Ghodichor Ferendrakumar S.	Info.Tech. Project Manag.	Computer Engineering	Assessment /evaluation process	28/05/2017
76	Dr. Chaudhary Dilip Damodar	Electronic Product Design	E & TC Engineering	Assessment /evaluation process	03/12/2016
77	Mr. .Gholap Sharad Balasaheb	Audio Video Engg.	E & TC Engineering	Assessment /evaluation process	27/05/2017
78	Mr. Dike Prashant Rajaramji	Broadband Communication	E & TC Engineering	Assessment /evaluation process	24/05/2017
79	Ms. Rajeshwarkar Vijaya Gopalrao	Integrated circuit	E & TC Engineering	Assessment /evaluation process	25/05/2017
80	Mr.Sonawane Pradnya Dadasaheb	Energy Audit & Management	E & TC Engineering	Assessment /evaluation process	25.01.2017

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