

List of supporting documents 2016-17

Sr. No.	Name of the workshops / seminar
1	seminar on “Entrepreneur Development”
2	Introduction to Patent filing in India
3	“Advanced Optimization Techniques and Applications in Electrical Engineering for Research”
4	Workshop on Patent Awareness
5	"Seminar on Research Grants & Funding".
6	STTP on “Report Writing Using LaTeX” for Research proposal writing
7	Seminar on “Skills and Knowledge Enhancement Program Entrepreneurship Development”.
8	Expert Guest Lecture on “Business in the real world”
9	Basics of INTELLECTUAL PROPERTY RIGHTS
10	Facts of Research Methodology
11	Workshop on entrepreneurship skills, attitude & Behaviour development



Sinhgad Technical Education Society's
SINHGAD INSTITUTE OF TECHNOLOGY

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Gat No. 309/310, Kusgaon (Bk), off Mumbai -Pune, Expressway.

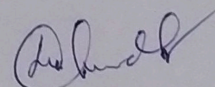
Lonavala, Pune, 410401, Website sit.sinhgad.edu

Department of Electronics & Telecommunication Engineering

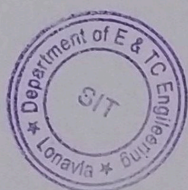
Date: 24thJun2017

NOTICE

All faculties are invited to attend seminar on “**Entrepreneur Development**” The seminar will be delivered on 28thJun 2017. Event is organized at B-108 (E&TC seminar hall), SIT, Lonavala. Interested faculties and students should contact to event coordinator Prof. P. A. Kamble before 27thJun 2017.



HOD



Brochure



SINHGAD TECHNICAL EDUCATION SOCIETY'S
SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA
AFFILIATED TO SAVITRIBAI PHULE PUNE UNIVERSITY
Phone: 02114- 673257,355-356

**Department of Electronics &
Telecommunication
Engineering**

**ORGANIZING SEMINAR
ON
“ ENTREPRENEUR
DEVELOPMENT ”**

**DATE: 28TH JUNE 2017
DAY: TUESDAY
VENUE: B-108 (E&TC SEMINAR HALL),
SIT, LONAVALA
TIME: 10:45AM TO 12PM**

Title:	Seminar on “Entrepreneur Development”
Date:	28 th June 2017
Day:	Tuesday
Venue:	B-108 (E&TC seminar hall), SIT, Lonavala
Time:	10:45am to 12pm
Staff Co-coordinator:	Prof. P. A. Kamble (Guest lecture coordinator), Dr. D.D. Chaudhary (IIC In charge) Prof. DipaliShende (IETE Incharge)
Event Coordinator:	Pooja Singh,
Event Conducted Under:	IIC Cell & IETE Student Forum, E&TC Dept, SIT, Lonavala
Activity Type:	Guest Lecture
Target Group:	All Students & Faculty Members of TE E&TC Department

Brief Summary about Activity:

A Guest Lecture on “**Entrepreneurship Development** ” was organized by IIC,EDC , Business club & IETE Student FORUM, E&TC Dept, SIT, Lonavala, for the Students of E&TC students and faculty .The speaker for the lecture was Mr. Suresh Umap who is regional officer in Pune at MCED . He provided the valuable guidelines for beginning and being successful Entrepreneur. He also made the students aware about different Entrepreneur schemes of Maharashtra Government and the benefits of it. Session was very much interesting and Interactive and mostly it was informative. Around 72 students and teachers attended the lecture and thoroughly enjoyed. And the event was a great success. The Event was a great success.

Photographs of Activity:



Speaker Felicitation



Interaction with student

Attendance:

SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA
DEPT. - *Electronics & Telecommunication Engg.*

Name of the Event :- Seminar on "Entrepreneur Development"
Date :- *28/06/2017*

Sr.No	Name of Participant	Sign
1	SHIRUDE PUSHPAK DILIP	<i>Shirude P</i>
2	BADGUJAR SARANG ASHOK	<i>Sarang Badgujar</i>
3	SHAHABAZ KHAN	<i>Shahabaz Khan</i>
4	SHINDE SHARAD VIJAY	<i>Shinde Sharad</i>
5	PALANDE SHUBHAM SAMIR	<i>Shubham Palande</i>
6	SONWANE SUSHANT GOUTAM (DA)	<i>Sushant Sonwane</i>
7	SUYASH CHITRANSHI	<i>Suyash Chitranshi</i>
8	AHIRRAO UMESH KISHOR	<i>Umesh Ahirrao</i>
9	AISHWARYA MANISEKARAN	<i>Aishwarya Manisekaran</i>
10	YOGESH DUBEY	<i>Yogesh Dubey</i>
11	AKSHAY YERUNKAR (DA)	<i>Akshay Yerunkar</i>
12	SONALI BASANTA KAR	<i>Sonali Basanta Kar</i>
13	AGARWAL AYUSHI SURESH	<i>Ayushi Agarwal</i>
14	SAMRAT PALI	<i>Samrat Pali</i>
15	SHANTANU M PACHARKAR	<i>Shantanu Pacharkar</i>
16	NAPHADE SHUBHAM ASHOK (DA)	<i>Shubham Naphade</i>
17	ANSHIKA SRIVASTAVA	<i>Anshika Srivastava</i>
18	PATIL PIYUSH	<i>Piyush Patil</i>
19	BHAGAT AKSHADA SHRIDHAR	<i>Akshada Bhagat</i>
20	KALNE VAIBHAV SHIVSHANKAR	<i>Vaibhav Kalne</i>
21	WAGHMARE TRUPTI DATTATRAYA	<i>Trupti Waghmare</i>
22	PURI ABHISHEK BABASAHEB	<i>Abhishek Puri</i>
23	NARHARE SACHIN NARSHINGRAO	<i>Sachin Narhare</i>
24	KAMBLE ASHOK KAMALAKAR	<i>Ashok Kamble</i>
25	DONGARE BHUSHAN PRAMOD	<i>Bhushan Dongare</i>
26	WAKODE AMRAPALI PRABHAKAR	<i>Amrapali Wakode</i>
27	PATIL SHUBHAM RAMESH	<i>Shubham Patil</i>
28	TAGADPALLEWAR TUSHAR TUKARAM	<i>Tushar Tagadpallewar</i>
29	NAVNEET KUMAR	<i>Navneet Kumar</i>
30	JIVIKA WASUDEO JAMGADE	<i>Jivika Jamgade</i>
31	PATIL SACHIN SUDAM	<i>Sachin Patil</i>
32	Rishabh Grover	<i>Rishabh Grover</i>
33	WAKALE SNEHALKUMAR AMBADAS	<i>Snehal Wakale</i>
34	KAPASE VAIBHAV VIJAY	<i>Vaibhav Kapase</i>
35	FAGARE SHRADDHA GURUNATH	<i>Shraddha Fagare</i>

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Lonavala, Pune, 410401,Website :sit.sinhgad.edu

Department of Electronics & Telecommunication Engineering

Sr.No	Name of Participant	Sign
36	KHEDKAR SONALI MAHADU	Sonali
37	SHRADDHA AGRAWAL	Shraddha
38	MEHUL DABHADE	Mehul
39	YADAV KARAN DINESH	Karan
40	SALUNKE JAYESH ANILBHAI	Jayesh
41	KADAM GYANBA VISHWANATH	Gyanba
42	AMIT SRIVASTAVA	Amit
43	KUMAWAT LAXMIKANT VASANT	Laxmikant
44	BHOI DIPAK SHANTARAM	Dipak
45	BHOSALE VISHAL VIDYADHAR	Vishal
46	MAHALE SHUBHAM ASARAM	Shubham
47	ABHISHEK KUMAR SINGH	Abhishek
48	KOMAL SINHA	Komal
49	KOPAL SNEHA	Kopal
50	SANGAVE PRATIKSHA VIJAYKUMAR	Pratiksha
51	NIKHIL Y KHANNA	Nikhil
52	YADAV MANISH	Manish
53	SONAWANE ANUJA NANDKUMAR	Anuja
54	PANKAJ JOSHI	Pankaj
55	KUNAL MAHAJAN	Kunal
56	MAGDUM VIKAS VASANT	Vikas
57	SHUBHRA MOTGILL	Shubhra
58	SIDDHANT GAUTAM TELANG	Siddhant
59	RICHA SHAH	Richa
60	SURAJ KUMAR	Suraj
61	JADHAV SHRINIVAS VITTHAL	Shrinivas
62	RISHABH PRASAD THAKUR	Rishabh
63	KHEDKAR AMOL BHIVSEN	Amol
64	SIDHQI RAFIQ JALEEL	Rafiq
65	KULKARNI RAHUL VISHWAS	Rahul
66	RAUT DEEPAKKUMAR PRAFULLA	Pratulla
67	TEJASWINI ADIKANE	Tejaswini
68	MANOHAR CHAUDHARY	Manohar
69	BHARTI DHOTE	Bharti
70	VIKAS KADAM	Vikas
71	NISHANT PACHPOR	Nishant
72	SALIM SHAIKH	Salim



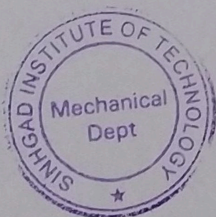
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Lonavala, Pune, 410401, Website : sit.sinhgad.edu
Department of Mechanical Engineering

Date: 11th June 2017

NOTICE

All faculties are invited Guest Lecture on “**Introduction to Patent filing in India**” The seminar will be delivered on 15th June 2017, event was organized at Seminar Hall, Department of Mechanical Engineering. Interested faculties and students should contact to event co-coordinator Mr. Sagar Wankhede before 14th June 2017.



HOD

Department of Mechanical Engineering.



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DEPARTMENT OF MECHANICAL ENGINEERING

INTRODUCTION TO PATENT FILING IN INDIA

**EVENT: TO CREATE AWARENESS ABOUT
PATENT FILING IN INDIA**

**VENUE: SEMINAR HALL, DEPARTMENT OF
MECHANICAL ENGINEERING**

DATE: 15 TH JUN, 2017
DURATION OF SESSION: 4 PM TO 5 PM

Name of Event: Introduction to Patent filing in India

Type of Event: Institute level Seminar for faculty.

Date: 15/06/2017

Venue: Seminar Hall, Department of Mechanical Engineering

Duration of session: 4 pm to 5 pm

Aim of Event: To create awareness about Patent filing in India

Description of Event:

The event is inaugurated by Dr. M. S. Gaikwad, Principal of SIT. The Principal has appreciated the initiative taken by the Mechanical Department, in his inaugural talk. He had explained the importance of the Patent filing. The session is conducted by Mr. A. A. Satpute and Dr. V. V. Shinde. The presentation is mainly to create awareness about Patent filing procedure in India. In presentation the focus was on 'How much the procedure is easy to file a patent in India'. In this we have discussed about the methods of filing patent, the cost required, location of patent offices, finding of patents, which can be and can't be file as patent etc. The session was conducted for 1 hr. in interaction type. Dr. V. V. Shinde has explained in nice manner about patent filing documentation.

We ended the session with conclusion that 'the Patent filing is easy but to invent something is quite difficult.'

Gallery: Session started



Welcome speech by Principal sir



Dr. V.V.Shinder delivering session

Attendance:

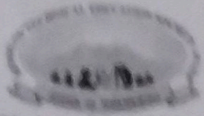
SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA
DEPT. - Mechanical Engineering

Name of the Event :- Introduction to Patent filing in India

Date :- 15/06/2017

Sr.No	Name of Participant	Sign
1	SHANTANU M PACHARKAR	Shantu
2	AMIT SRIVASTAVA	Amit
3	KARNALE SURAJ	Suraj
4	JIVIKA WASUDEO JAMGADE	Jamgade
5	PATIL SACHIN SUDAM	Sachin
6	rishabh Grover	Rishabh
7	SUYASH CHITRANSHI	Suyash
8	YEDE KIRTI HANUMAN	Kirti
9	SHIRUDE PUSHPAK DILIP	Shirude
10	KULKARNI RAHUL VISHWAS	Rahul
11	MAGDUM VIKAS VASANT	Magdum
12	KUNAL MAHAJAN	Kunal
13	WAIKAR ROHAN RAJENDRA (DA)	Waikar
14	GABHANE VAIBHAV VIKAS	Vaibhav
15	YOGESH DUBEY	Yogesh
16	ABHISHEK CHOUKSEY	Abhishek
17	WARULE RUTUJA SHARAD	Rutuja
18	KUMAWAT LAXMIKANT VASANT	Laxmi
19	DONODE AKSHAY MORESHWAR	Donode
20	YADAV SHUBHAM ASHOK	Shubham
21	GANGAR MANAN JITENDRA	Manan
22	YADAV HARSHADA PRADEEP	Harshada
23	AKASH CHOUDHARY	Akash
24	YADAV TULJESH NANDLAL (DA)	Tuljesh
25	DHANAWADE MOULSHREE SUHAS	Mouli
26	ADHUDE VANDNA RAOSAHEB	Vandna
27	BHISE AISHWARYA SHARAD	Bhise
28	KALNE VAIBHAV SHIVSHANKAR	Kalne
29	SUYASH CHITRANSHI	Suyash
30	DIGHE CHETANKUMAR MACHHINDRA	Dighe
31	KADAM GYANBA VISHWANATH	Kadam
32	GAUR RAHUL RAMSWARUP	Rahul
33	KAMBLE ASHOK KAMALAKAR	Kamble
34	BHUTKAR AKASH BABANRAO	Bhutkar
35	RAMPHALE AISHWARYA	Ramphale

Sr.No	Name of Participant	Sign
36	PATHAN ISRAK KHAN RASHID KHAN	<i>Pathan</i>
37	SURAJ KUMAR	<i>Suraj</i>
38	PATIL DIKSHA NITIN	<i>Diksha</i>
39	UDDESHYA PRAKASH	<i>Uddeshya</i>
40	VIVEK KUMAR	<i>Vivek</i>
41	YEDE KIRTI HANUMAN	<i>Yede</i>
42	DESHMUKH SANKALP GIRISH	<i>Pesha</i>
43	PRIYA KESHRI	<i>Pesha</i>
44	RATHOD BHIMU NAMDEV	<i>Rathod</i>
45	SMIT TOPIWALA	<i>Smit</i>
46	PATIL AVINASH ARUN	<i>Patil</i>
47	MITKARI SHUBHAM	<i>Shubham</i>
48	WANI SHUBHAM KASHINATH	<i>Shubham</i>
49	DHAGADI OMPRASAD MAHADEO	<i>Dhagadi</i>
50	AISHWARYA MANISEKARAN	<i>Aishwarya</i>
51	JASLEEN SUNIL HUMNE	<i>Jasleen</i>
52	DIGHE CHETANKUMAR MACHHINDRA	<i>Dighe</i>
53	WALKE SANKET VISHNU	<i>Walke</i>
54	DHEERAJ KUMAR	<i>Dheeraj</i>
55	TAGADPALLEWAR TUSHAR TUKARAM	<i>Tagadpallewar</i>
56	BORSE NEELAM RAJARAM	<i>Borse</i>
57	MISHRA YASH SHIV	<i>Mishra</i>
58	RAKSHIT RAJ	<i>Rakshit</i>
59	YEDE KIRTI HANUMAN	<i>Kirti</i>
60	GUPTA POORNIMA DEVANAND	<i>Poornima</i>
61	HAVELE SHRUTI SANJAY	<i>Havele</i>
62	IDHATE RUPALI TUKARAM (DA)	<i>Rupali</i>
63	MANOHAR CHAUDHARY	<i>Manohar</i>
64	RUPALI MASKE	<i>Rupali</i>
65	RUPALI SHISHUPAL	<i>Rupali</i>
66	VIKAS KADAM	<i>Vikas</i>
67	BHARTI DHOTE	<i>Bharti</i>
68	NISHANT PACHPOR	<i>Nishant</i>
69	SALIM SHAIKH	<i>Salim</i>
70	VAISHALI DHAWAS	<i>Vaishali</i>



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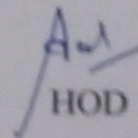
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Department of Electrical Engineering

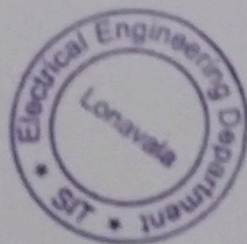
Date: 1 March 2017

NOTICE

All students are invited to attend "Advanced Optimization Techniques and Applications in Electrical Engineering" On 06th to 10th March 2017, Organized by Electrical Department SIT Lonavala. Interested students should contact to event coordinator Prof. S. V. Tade before 05th March 2017.


HOD

Department of Electrical.



Brochure:



Sinhgad Institutes

Sinhgad Technical Education Society's
Sinhgad Institute of Technology, Lonavala
One week short term training programme

ON

Advanced Optimization Techniques and Applications in Electrical Engineering

06th to 10th March 2017

Convener

Dr. Amol A. Kalage

Co-ordinator
Prof. Manohar N. Kalgunde
Prof. Sarika V. Tade

Organization Secretary
Prof. Santosh L. Mhette

Organized by

Department of Electrical Engineering
SINHGAD INSTITUTE OF TECHNOLOGY
GAT NO - 309/310, KUSGAON (BK), LONAVALA,
TAL- MAVAL, DIST- PUNE, PIN - 410 401, (MS)
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Fax : 02114-278304

About SIT

Sinhgad Institute of Technology (SIT), Lonavala has been set up by Sinhgad Technical Education Society, Pune under the leadership of its founder president Prof. M.N. Navale.

The institute is located at an exquisite hill-station Lonavala in Maharashtra. Over 160 acres of the campus is beautifully carved atop the hills in Sahyadri mountain ranges. The all fresh environment is complemented by marvellous scenic beauty.

Since its inception in 2004, the institute has been built on strong foundations of the best teaching-learning practices. The all-round growth of students is the centre of focus of all the activities and programs conducted at SIT. Academics is given the topmost importance and its conduct is based on the perfected processes of planning, execution, monitoring and accountability.

Value addition programs are meticulously organized to enhance competency and soft skills of the students. Understanding the importance of faculty development, the institute strives hard to promote faculty interaction with industry.

How to reach SIT

STES Lonavala campus is very well connected to Mumbai and Pune by Road, Rail and Air as well. It is almost midway between Mumbai and Pune airports, approximately 80 Km.



The picturesque STES campus at Lonavala
(A panorama from Pune-Mumbai Expressway)



Patrons

Hon. Prof. M. N. Navale,
Founder President, STES, Pune
Dr. (Mrs) S. M. Navale,
Founder Secretary, STES, Pune
Mr. Rohit M. Navale,
Vice President (HR), STES, Pune.
Mrs. Rachana M. Navale-Ashokar,
Vice President/Admin, STES, Pune.
Dr. M. S. Gaikwad,
Principal, SIT, Lonavala.

Convener

Dr. A. A. Kalage 8605033245
Head, Department of Electrical Engineering.

Organizing Secretary

Prof. S. L. Mhette 9921838393

Co-Ordinator

Prof. Ms. S. V. Tade 8605165256
Prof. M. N. Kalgunde 9422421543

ORGANIZING COMMITTEES

Inauguration

Prof. Ms. P. D. Sonawane 8454040182
Prof. Ms. M.U. Naik 8007775294

Technical Sessions

Dr. R. Sheshbhaglakshmi 9011066137
Prof. R. M. Narkar 727698484
Prof. C.M. Subhmanayam 9049839059

Hospitality

Prof. J. A. Khebgade 9403979406
Prof. N. M. Rao 9689807422

Publications and Publicity

Prof. G. R. Walke 8976002105
Prof. S. B. Jadhav 9006098371

Logistics

Prof. A. V. Tahmane 9975200017
Prof. D. M. Chahyal 9960183686



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Department of Electrical Engineering

STTP Schedule

ADVANCED OPTIMIZATION TECHNIQUES
WITH APPLICATIONS IN ELECTRICAL ENGINEERING
(06.03.2017 to 10.03.2017)
SCHEDULE

	MONDAY	TUESDAY	WEDNESDAY	THURSDAY	FRIDAY
09.30-11.00 am	Registration & Inauguration	Harmonic Search Algorithm (Dr. A G Kamble)	Advanced TLBO (Dr A A Kalage)	Genetic Algorithm (Dr. P P Bedekar)	Dynamic Program (Prof A S Shinde)
11.00-11.15 am	BREAK				
11.15-12.45 pm	Linear Programming Problem (Dr N D Ghawaghawe)	TLBO (Dr G G Waghmare)	Advanced TLBO (Dr A A Kalage)	Genetic Algorithm (Dr. P P Bedekar)	Dynamic Program (Prof A S Shinde)
12.45-01.45 pm	LUNCH				
01.45-03.15 pm	Analytic Hierarchy Method (Dr. A G Kamble)	JAYA Algorithm (Dr. A G Kamble)	PSO (Dr. P M Sonawane)	ABC (Dr H T Jadhav)	Valedictory
03.15-03.30 pm	TEA				
03.30-05.00 pm	MADM Methods (Mrs S V Tade)	Lab Session (Mrs. S V Tade)	PSO (Dr. P M Sonawane)	ABC (Dr H T Jadhav)	Sight Seeing



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Department of Electrical Engineering

A STTP report on “Advanced Optimization Techniques and Applications in Electrical Engineering”

The event “**Advanced Optimization Techniques and Applications in Electrical Engineering**” organized by Electrical Department was started at **10am**. On **06th to 10th March 2017**. The event was organized at corporate training Center, **CTC**. The **total no of participants** were 25.

Day 01 : 06th March 2017

Dr.M.S.Gaikwad, Principal, SIT Lonavala, and Dr.A.A.Kalage, HOD, Electrical Dept., jointly inaugurated STTP. By addressing the crowd with a word of guidance, he very clearly and eloquently described and emphasised the idea, spirit, and motivation of this action. The day's first session began with an expert talk by Dr. N.D.Ghavghawe. He went over all of the concepts and flow of Advanced optimization techniques from a conceptual standpoint, as well as his way for teaching some tough linear programming portions. Dr. A.G. Kamble from SIT Lonavala led the second session. He went over AHP, which is one of the optimization methods. Dr.A.G.Kamble conducted the third session on AHP and MADM approaches.

Day 02 : 07th March 2017

The session was conducted by Dr.G.G.Waghmare from KKW college, Nashik. Teaching Learning Optimization Techniques was one of the topics he discussed. He shared his experiences with the audience. Dr. A.G. Kamble from SIT Lonavala led the second session. Jaya Algorithm was explained by him. Prof. S.V.Tade from SIT Lonavala led the third session on MADM techniques..

Day 03: 08th March 2017

The session was conducted by Dr.A.A.Kalage from SIT, Lonavala. He discussed about Advanced Teaching learning optimization Techniques. Shared his experiences with participants. The 2nd session was conducted by Dr. P.M.Sonavane from KKW, Nasik. He explained about Particle Swarm Optimization (PSO).3rd session was conducted by Dr.A.A.Kalage from SIT, Lonavala. He discussed about Genetic Algorithm.

Day 04 : 09th March 2017



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Department of Electrical Engineering

Dr.P.P.Bedekar of Govt. College of Engg. Amaravati led the discussion. He went over the toolset for Genetic Algorithms. He talked about his experiences with the audience. Prof. Bamane of RIT, Sakhrale, led the second session. He discussed the concept of a neural network. Dr.P.P.Bedekar of Govt. College of Engg. Amaravati led the third lab session on GA toolbox.

Day 05 : 10th March 2017

Prof. A.S. Shinde of the Govt. College of Engineering in Amaravati led the discussion. He talked about Dynamic Programming (Dynamic Programming). He shared his experiences with the audience. Prof. A.S. Shinde from Govt. College of Engg. Amaravati led the second session on Dynamic Programming. In the third session, there is a validation function.



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Department of Electrical Engineering

Event Photos



Session



Session

Sinhgad Technical Education Society's
Sinhgad Institute of Technology, Lonavala
Department of Electrical Engineering
Organizes

**SHORT TERM TRAINING PROGRAM ON
"ADVANCED OPTIMIZATION TECHNIQUES WITH
APPLICATIONS IN ELECTRICAL ENGINEERING"**

6th - 10th MARCH 2017

**VENUE: CORPORATE TRAINING CENTER (CTC)
SINHGAD INSTITUTES, LONAVALA**

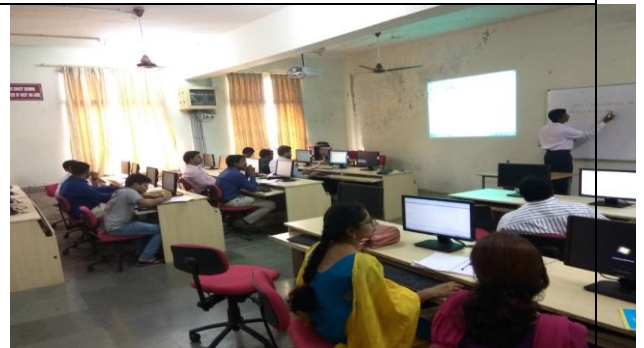
DESIGNED BY: TUSHAR AULTI



Session



Session



Session



Sinhgad Institutes

SINHGAD TECHNICAL EDUCATION SOCIETY'S

SINHGAD INSTITUTE OF TECHNOLOGY

(Affiliated to Savitribai Phule Pune University, Pune & Approved by AICTE)

Gat No. 309/310, off Mumbai Pune Expressway Kusagaon (Bk), Lonavala Pune - 410401

Department of Electrical Engineering

Sinhgad Institute of Technology, Lonavala

Department of Electrical Engineering

One week STTP on "Advanced Optimization Techniques with Applications in Electrical Engineering"

Attendance Sheet

Sr. No	Name of the Participants	6/3/2017	7/3/2017	8/3/2017	9/3/2017	10/3/2017
1	Hardik Babulal Patel	Patel	Patel	Patel	Patel	Patel
2	Mantra Nitinbhai Patel	mnpatel	mnpatel	mnpatel	mnpatel	mnpatel
3	Gunesh Gajendrasinh Thakor	Thakor	Thakor	Thakor	Thakor	Thakor
4	Kirit Mansukhbhai Hothi	Hothi	Hothi	Hothi	Hothi	Hothi
5	Ajay Rajendrabhai Bosamiya	Bosamiya	Bosamiya	Bosamiya	AB	Bosamiya
6	Jadhav Girish V.	Jadhav	Jadhav	Jadhav	Jadhav	Jadhav
7	Bhushan Kadam	Kadam	Kadam	Kadam	Kadam	Kadam
8	Sumit Pathade	Pathade	Pathade	Pathade	Pathade	Pathade
9	Sarvesh Kumar	Pathade	Pathade	Pathade	Pathade	Pathade
10	Parveen Kumar	Parveen	Parveen	Parveen	Parveen	Parveen
11	Sadab Mulla	Sadab	Sadab	Sadab	Sadab	Sadab
12	Lokesh Badgujar	Lokesh	Lokesh	Lokesh	Lokesh	Lokesh
13	Akshay Kale	Akale	Akale	Akale	Akale	Akale
14	Girish Chaudhari	Chaudh	Chaudh	Chaudh	Chaudh	Chaudh
15	Nilesh Bonde	NBonde	NBonde	NBonde	NBonde	NBonde
16	Minal Kokare	Mkokar	Mkokar	Mkokar	Mkokar	Mkokar
17	Atul Ballal	Aballal	Aballal	Aballal	Aballal	Aballal
18	Mandar Dongare	AB	Dong	Dong	Dong	Dong
19	Mayuri Manikshetti	Mm	Mm	Mm	Mm	Mm
20	Kiran Gitte	Kgitte	Kgitte	Kgitte	Kgitte	Kgitte



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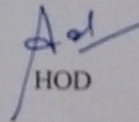
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www.sit.sinhgad.edu

Department of Electrical Engineering

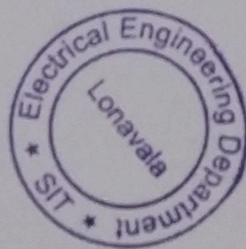
Date: 10 FEB, 2017

NOTICE

All faculties are invited Workshop on "PATENT AWARENESS" event was organized at 16 FEB 2017, SIT, Lonavala. Interested faculties and students should contact to event coordinator Mr. M. S. Shinde, Assistant Professor, Electrical Engineering Dept, before 15 FEB, 2017.


HOD

Department of Electrical Engineering.



Brochure:



**SINHGAD TECHNICAL EDUCATION SOCIETY'S
SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA**

**Department Of Electrical Engineering
Organizing**

WORKSHOP ON PATENT AWARENESS

16 FEB 2017

2.00PM TO 5.00PM

VENUE: Electrical Seminar Hall

Co-ordinator: M.S.Shinde



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Lonavala, Pune, 410401, Website : sit.sinhgad.edu

Department of Electrical Engineering

Report- Workshop on “PATENT AWARENESS

Department of Electrical Engineering has organized one day workshop on

Name of Event : **“PATENT AWARENESS”**

Date: 16 FEB, 2017.

Time: 2pm To 5pm

Total 25 participants have attended the workshop.

The resource person for the workshop was Mr. Vivek Deshpande, Professor. MIT Kothrud.

Topic covered: Introduction to IPR and types.

Patent, Copyright, Design, Trademark

Casestudies

Patent filing procedure

Session was very informative. Benefited to faculties and students. 25 participants attended session

Photo



Session going on

Attendance

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Gat No. 309/310, off Mumbai Pune Expressway, Kusgaon (Bk), Lonavala Pune - 410401
Department of Electrical Engineering

Event Attendance

Name of the Event	Workshop on "PATENT AWARENESS"	
Date	18 FEB 2017	

Sr.No	Name of Participant	Sign
1	AVACHAT HARISH SUNIL	<i>[Signature]</i>
2	KAMBLE RUCHITA DILIP	<i>[Signature]</i>
3	BHUSKAR ASHWINI UDAYRAO	<i>[Signature]</i>
4	KHEDKAR ASHWAJITA VIJAY	<i>[Signature]</i>
5	SADU THIRMALA RAO VASUDEORAO	<i>[Signature]</i>
6	JORALE TUSHAR BALASAHEB	<i>[Signature]</i>
7	KADAM KIRTI UDAY	<i>[Signature]</i>
8	KADAM DINKAR	<i>[Signature]</i>
9	KADAM ROHINI	<i>[Signature]</i>
10	KAJALE ANIKET RAVINDRA	<i>[Signature]</i>
11	KALE PUJA NAMDEO	<i>[Signature]</i>
12	KANDARKAR GAURAV PRAMOD	<i>[Signature]</i>
13	KHANDARE VISHAL RAJENDRA	<i>[Signature]</i>
14	KHETRE SUMIT ARUN	<i>[Signature]</i>
15	KOKANE ABHILASH PRAVIN	<i>[Signature]</i>
16	KHOMANE ATUL	<i>[Signature]</i>
17	LIJITKAR RASHMI GAUTAM	<i>[Signature]</i>
18	LOHKARE AMBIKA UDDHAV	<i>[Signature]</i>
19	MANE PRAGATI UTTAM	<i>[Signature]</i>
20	MORE AJAY ANIL	<i>[Signature]</i>
21	NIKAM KULDEEP SUNIL	<i>[Signature]</i>
22	PATEL PRASHANT CHAMPALAL	<i>[Signature]</i>
23	PATEL PRIYANKA ANNASO	<i>[Signature]</i>
24	PAWAR DARSHAN SANJAY	<i>[Signature]</i>
25	POTALE SOPAN VITTHAL	<i>[Signature]</i>



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Department of Electronics & Telecommunication Engineering

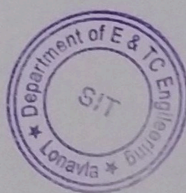
Date: 11th February, 2017

NOTICE

All faculties are invited to attend, "Seminar on Research Grants & Funding" organized under e-sitizen Club. 15th February, 2017, event was organized at SIT D102 (Mech. Seminar hall), Lonavala Campus. Interested faculties and students should contact to event co-coordinator Mrs. P. A. Kamble & Mrs. Dipali K. Shende before 14th February, 2017.

HOD

Department of E&TC Engineering



Brochure



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Department of Electronics & Telecommunication

SEMINAR ON RESEARCH GRANTS & FUNDING

Mr. Pradeepkumar Dhage
from MIT Technology Business Incubators Pune.

15th February, 2017
03:00 pm to 05:00 pm
SIT D102 (Mech. Seminar hall),
Lonavala Campus

The session was organized in order to increase knowledge of grants & funding in the field of research among the faculty members.

Event Report

Event :	
TITLE:	Seminar on Research Grants & Funding
Date:	15 th February, 2017
Day:	Wednesday
Venue:	SIT D102 (Mech. Seminar hall), Lonavala Campus
Time:	03:00 pm to 05:00 pm
Staff Co-ordinators:	1) Prof. P. A. Kamble [Guest Lecture-Incharge (E&TC)] 2) Prof. Dipali K. Shende [IETE Faculty In-charge (E&TC)]
Student Co-ordinators:	1. Pratik Soumaiya 2. Subodh Digraskar and IETE Committee
Event Conducted Under:	IETE Student Chapter, SIT, Lonavala
Activity Type:	Seminar
Target Group:	Staff of SIT Lonavala
Brief Summary about Activity:-	

IETE Student Forum organized a expert session on “Research Grants & Funding” on 15th Feb 2017. The guest for the session was Mr. Pradeepkumar Dhage from MIT Technology Business Incubators Pune.

The session was organized in order to increase knowledge of grants & funding in the field of research among the faculty members.

The key points of session were-

- What is TBI (Technology Business Incubators) and what does it do?
- Different national funding agencies.
- Different international funding agencies.
- Detail about DST and its operations
- Project sponsorship
- Operations of NIDHI(National Institute of Development & Harnessing Innovation)
- Parameters required to setup a TBI
- Case study on How to write a proposal



The session was delivered in an excellent manner and more than 35 faculties participated in the event.

Photographs of Activity

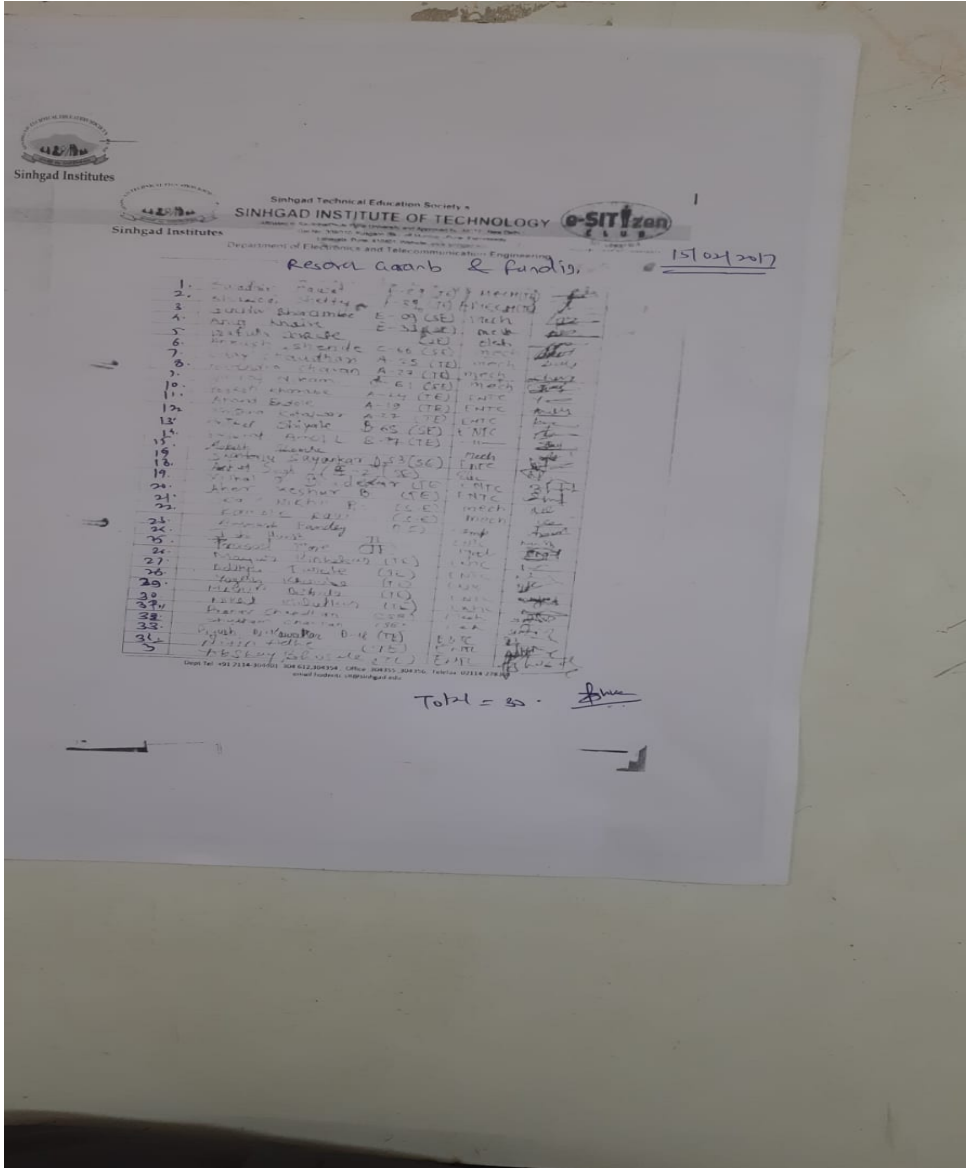


Felicitation of guest by Principal Dr..M.S.Gaikwad

Inauguration of Event

: 35

Attendance



Resara Gaarb & Fundis 15/02/2017

Sl. No.	Name	Roll No.	Branch	Signature
1.
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Total = 35 *[Signature]*



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Department of Electronics & Telecommunication Engineering

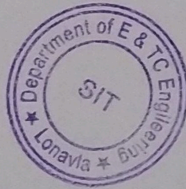
Date: 13th Nov, 2016

NOTICE

All faculties are invited to attend **STTP on "Report Writing Using LaTeX"**. The seminar will be delivered on November 18-19th, 2016, event is organized at B-201, E&Tc Dept. Interested faculties and students should contact to event co-coordinator Mrs. Dipali Shende before 17th November 2016.

HOD

Department of E&TC Engineering





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Department of Electronics & Telecommunication

STTP ON “REPORT WRITING USING LATEX”

Dr. T. J. Parvat, SIT, Lonavala.
Prof. A. V. Nadargi, SIT, Lonavala

November 18-19, 2016
09.00 pm to 5.00 pm
B-201, E&Tc Dept

This STTP aims to provide a platform for adding basic skills in report writing. LaTeX encourages academicians and not to worry too much about the appearance of their documents but to concentrate on getting the right content.

Department of Electronics & Telecommunication Engineering

Title:	STTP on “Report Writing Using LaTeX”
Date:	November 18-19, 2016
Day:	Friday and Saturday
Venue:	B-201, E&TcDept
Time:	09.00 pm to 5.00 pm
Staff Coordinator:	Prof. D. S. Mantri, Prof. N. N. Pachpor, Prof. G. V. Lohar Prof. A. A. Labade, Prof. G. A. Bhalerao, Prof. M. S. Jadhav Mr. V. D. Raskar, MS. A. C. Sawant
Student coordinators:	Adnan Shaikh, Vivaan Kumar, Bhakti Chaudhari, Santosh Shinde, Saiprasad Sisale, Shubham Urade & team.
Event Conducted Under:	IE Club, SIT, Lonavala.
Activity Type:	STTP
Target Group:	Master students and faculty
Resource Person	Dr. T. J. Parvat, SIT, Lonavala. Prof. A. V. Nadargi, SIT, Lonavala

Brief Summary about Activity:

This STTP aims to provide a platform for adding basic skills in report writing. LaTeX encourages academicians and not to worry too much about the appearance of their documents but to concentrate on getting the right content. This STTP in turn creates interest and promote faculty to work in area where accuracy and presentation of document is important. This in turn creates interest and promote student and faculty to use LaTeX tool for preparation of technical reports. It also helps to encourage all faculties, Ph.D. and M.E. aspirants of esteemed organizations to improve their skills.

Photographs of Activity :



Speaker Felicitation

Interaction with student

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Department of Electronics & Telecommunication Engineering

Attendance:

SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Name OF DEPT. - *Electronics & Telecommunication Engg*

Name of the Event :- *STTP on "Report Writing Using LaTeX"*

Date :- *18/11/2016*

Sr.No	Name of Participant	Sign
1	ABHISHEK CHOUKSEY	<i>[Signature]</i>
2	CHIKHALE PALLAVI RATNAKANT	<i>[Signature]</i>
3	PATHAN ISRAK KHAN RASHID KHAN	<i>[Signature]</i>
4	POOJA GUJARATHI	<i>[Signature]</i>
5	PURI ABHISHEK BABASAHEB	<i>[Signature]</i>
6	RAKSHIT RAJ	<i>[Signature]</i>
7	BORSE NEELAM RAJARAM	<i>[Signature]</i>
8	ANNADATE SHUBHAM DAYANAND	<i>[Signature]</i>
9	NAGARGOJE KIRANKUMAR EKNATHRAO	<i>[Signature]</i>
10	SMRITI SUMAN	<i>[Signature]</i>
11	SACHAPARA VIVEK DINESHBHAI	<i>[Signature]</i>
12	BORSE PRASHANT SANTOSH	<i>[Signature]</i>
13	SHARMA MUSKAN RAJENDRA	<i>[Signature]</i>
14	SIDDHIQI RAFIQ JALEEL	<i>[Signature]</i>
15	SIHARAN OMPRAKASH MOURYA	<i>[Signature]</i>
16	GADEKAR ANIL MADHAV	<i>[Signature]</i>
17	VINERKAR SAURABH SHASHIKANT	<i>[Signature]</i>
18	WAGHMARE TRUPTI DATTATRAYA	<i>[Signature]</i>
19	PAWASKAR OMKAR MANGESH	<i>[Signature]</i>
20	KALEKAR ANURADHA DILIP	<i>[Signature]</i>
21	YEDE KIRTI HANUMAN	<i>[Signature]</i>
22	ADISH JAIN	<i>[Signature]</i>
23	DONGARE SAURABH ARUN	<i>[Signature]</i>
24	AISHWARYA MANISEKARAN	<i>[Signature]</i>
25	SHUBHAM MAURYA	<i>[Signature]</i>
26	DANGE TEJASWINI BHASKAR	<i>[Signature]</i>
27	NISHANT PACHPOR	<i>[Signature]</i>
28	SALIM SHAIKH	<i>[Signature]</i>
29	VAISHALI DHAWAS	<i>[Signature]</i>
30	BHARTI DHOTE	<i>[Signature]</i>



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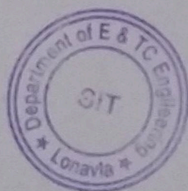
Date: 19th August 2016

NOTICE

All faculties are invited to attend Seminar on "Skills and Knowledge Enhancement Program". The seminar will be delivered on 23th August 2016. Time: 11.00 AM To 1.00 PM Seminar will organized at IT seminar hall. Interested faculties and students should contact to event co-coordinator Mrs. Prof. J. R. Gangane before 22th August 2016.

HOD

Department of E&TC Engineering.



Brochure



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Phone: 02114- 673257,355,356

Department of Electronics & Telecommunication Engineering

ORGANIZING SEMINAR ON

SKILLS AND KNOWLEDGE ENHANCEMENT PROGRAM

**23TH AUGUST 2016
11:00AM TO 1:00PM
IT SEMINAR HALL**

The speakers for the seminar: Prof.Kiran Talele, Prof.Abhay Phansikar,
Dr.B.Satyanarayana





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Lonavala, Pune, 410401,Website :sit.sinhgad.edu

Department of Electronics & Telecommunication Engineering

Title:	Seminar on “Skills and Knowledge Enhancement Program”
Date:	23 th August 2016
Day:	Saturday
Venue:	IT seminar hall

Department of Electronics & Telecommunication Engineering

Time:	11:00am to 1:00pm
Staff Co-coordinator:	
Event Conducted Under:	
Event Conducted Under:	
Activity Type:	
Target Group:	
Resource Person:	
Brief Summary about the Event:	<p>A seminar on "Skill Enhancement Program" was conducted on 23/08/2016. The seminar was held in the Department of Electronics & Telecommunication Engineering. The resource person for the seminar was Prof. Kiran Talele, Pimpri Chinchwad Education Trust, Pimpri. The event was a great success.</p>
Photographs of Activity:	 
Remarks:	

DEPARTMENT OF ELECTRONICS & TELECOMMUNICATION ENGINEERING

"Seminar on Skills and Knowledge Enhancement Program".

23/08/2016

Attendance Sheet

Sl. No.	Roll No.	Name	Sign	Sl. No.	Roll No.	Name	Sign
1	SE401	AKARSHI SINGH CHAUHAN	[Signature]	37	SE437	NIMGADE PRAGATI VIJAY	[Signature]
2	SE402	KRITYA DHORE	[Signature]	38	SE438	PALASH JAIN	[Signature]
3	SE403	AGALE SACHIN RAMBHAI	[Signature]	39	SE439	PATIL AMAR APPASAHEB	[Signature]
4	SE404	AHIRE PAVAN GORAKH	[Signature]	40	SE440	PATIL ROHIT SANJAY	[Signature]
5	SE405	AKSHAY R LOHAKARE	[Signature]	41	SE441	PATIL SNEHAL HAMBIRAO	[Signature]
6	SE406	ANVITA DEWENDRAJI JAKHARAJI	[Signature]	42	SE442	PAWAR SHUBHAM BABURAO	[Signature]
7	SE407	CHAKRANYA BHARDWARAJ	[Signature]	43	SE443	PETKAR PRANITA DEWANAND	[Signature]
8	SE408	CHANDAN KUMAR	[Signature]	44	SE444	PODIA SINGH	[Signature]
9	SE409	CHINMAY SURENDRK JANKAR	[Signature]	45	SE445	PRATYUSH	[Signature]
10	SE410	DESAI AJINKYA VILASRAO	[Signature]	46	SE446	ROY SAHIL SANJAY	[Signature]
11	SE411	DEVESH SUSHIL BHAVSAR	[Signature]	47	SE447	SARTHAK SINGH	[Signature]
12	SE412	DEVSHREE SAHAKAR	[Signature]	48	SE448	SARWAT RIYAJA SHARAD	[Signature]
13	SE413	DEWANE ANSHU TUSHI	[Signature]	49	SE449	SHILAR TUKARAM VITTHAL	[Signature]
14	SE414	DROVIDYAN VYAS	[Signature]	50	SE450	SHINDHE SAKSHI	[Signature]
15	SE415	DUGADE SHIVAM ASHOK	[Signature]	51	SE451	SHEWALE SURAJ DNYANDEV	[Signature]
16	SE416	GAWADE MANGESH ASHOK	[Signature]	52	SE452	SHIVANE BRINGADE	[Signature]
17	SE417	GHANGDE SAURABH ASHOK	[Signature]	53	SE453	SHIVANI KUMARI	[Signature]
18	SE418	SHIVANI ARYAN	[Signature]	54	SE454	SHIVANI NAGAR	[Signature]
19	SE419	JADHAV PRATYUS SAHEBRAO	[Signature]	55	SE455	SURYAWANSHI SHIVANI SUNIL	[Signature]
20	SE420	JADHAV PRAMOD GOVIND	[Signature]	56	SE456	TAXADE ANIKET RAH	[Signature]
21	SE421	JAYSHREE SUNISHI	[Signature]	57	SE457	VARPE KAVI SUNE	[Signature]
22	SE422	KHANDLWAL NISHU UTTAM	[Signature]	58	SE458	VIBHUTE SHIVANI KISHAN	[Signature]
23	SE423	KINAGE SAKUL DILIP	[Signature]	59	SE459	WADKAR SURESH	[Signature]
24	SE424	KOKANE VIKAS PANDURANG	[Signature]	60	SE460	WADKARMANISH KAILAS	[Signature]
25	SE425	KRISHNA KINARE	[Signature]	61	SE461	VEGLE SANKET VIJAY	[Signature]
26	SE426	KUMAR ANSHU TUSHI	[Signature]	62	SE462	VISHANT	[Signature]
27	SE427	KUMAR ARJUN	[Signature]	63	TE A 29	LOHE RAJESH RAMRAO	[Signature]
28	SE428	KUMAR PRADIP	[Signature]	64	TE A 30	PANDEY SWATI RAJENDRAKUMAR	[Signature]
29	SE429	KUNAL KISHORE	[Signature]	65	TE A 31	PATIL SHUBHAM MOTIRAM	[Signature]
30	SE430	KUNDE PRANESH SURESH	[Signature]	66	TE A 32	POPHALE SAJISH EADENATH	[Signature]
31	SE431	MADHUKAR SIDDHESHWAR	[Signature]	67	TE A 33	PAUL AKSHAY RAMSHI	[Signature]
32	SE432	MANUJA MAHESHNAIK	[Signature]	68	TE A 34	RELEKAR VIKRUP VINAY	[Signature]
33	SE433	MELLE DAYANAND ASHOK	[Signature]	69	TE A 35	RINGANE SAKSHI RAMBHAI	[Signature]
34	SE434	NIBHAL BALAJIRANE	[Signature]	70	TE A 01	AHIRE KISHAV BHANUDAS	[Signature]
35	SE435	NIKHIL CHAUDHAN	[Signature]				
36	SE436	NIKITA MAHADEV YADAV	[Signature]				

Attendance: 70



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Lonavala, Pune, 410401. Website sit.sinhgad.edu
Department of Electronics & Telecommunication Engineering

Date: 10 July, 2016

NOTICE

All faculties are invited to attend Expert Guest Lecture on "Business in the real world" The expert guest lecture will be delivered on 14 July, 2016, event will organized at B-104, E&TC Dept., SIT, Lonavala. Time: 4.00 PM to 5.00 PM. Interested faculties and students should contact to event co-coordinator mrs. P.A.kamble before 13 jully, 2016.

HOD

Department of E&TC Engineering.





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Department of Electronics & Telecommunication Engineering

ORGANIZING EXPERT GUEST LECTURE ON

BUSINESS IN THE REAL WORLD

14 JULY, 2016
4 PM TO 5 PM

VENUE: B-104, E&TC Dept., SIT, Lonavala.



Prof.P.A.Kamble
Coordinator

Dr. D.D.Chaudhary
Vice-Principal

Dr. M.S.Gaikwad
Director & Principal

Event Report

EVENT NO:- 02
Title: Expert Guest Lecture on ‘Business in the real world’
Date: 14 July, 2016
Day: Friday
Venue: B-104, E&TC Dept., SIT, Lonavala.
Time: 4 pm to 5 pm
Staff Co-coordinator: Prof.P.A.Kamble
Event Conducted Under: Pranay Chambhare
Vivek Singh
Samantha Gupta
Event Conducted Under: IEEE STUDENTS CHAPTER
Activity Type: Guest Lecture
Target Group: All Students of BE E&TC Department

Brief Summary about Activity:

A GUEST LECTURE on “Business in the real world” was organized by IEEE Student Branch, E&TC Dept, SIT, Lonavala, for the Students of BE E&TC students & Researchers. The speaker for the lecture was Mr. Adnan Ghadiyali. Around 50 students and teachers attended the lecture. Prof.P.A.Kamble was the Guest Lecture coordinator and all the Members of IEEE Student Branch, SIT Lonavala were the student coordinators for the event.

The Event was a great success.

Photographs of Activity:



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Attendance:50

Sinhgad Technical Education Society's SINHGAD INSTITUTE OF TECHNOLOGY

(Affiliated to SavitribaiPhule Pune University, Pune and Approved by, AICTE, New Delhi.)
Gat No. 309/310 ,Kusgaon (Bk), off Mumbai –Pune, Expressway.

Lonavala, Pune, 410401,Website : sit.sinhgad.edu

Department of Electronics & Telecommunication Engineering

Event Attendance		
Name Of Dept:		
Name of the Event	Expert Guest Lecture on " Business in the real world".	
Date	14/07/2016	
Sr.No	Name Of Participant	Sign
1	Prof. P. A. Kamble	<i>PK</i>
2	Prof. S. B. Gholap	<i>SBG</i>
3	Prof. V.S.Baste	<i>VSB</i>
4	Prof. M. S. Jadhav	<i>MJ</i>
5	Bhatt Aakanksha Ravikumar	<i>AR</i>
6	Bhosle Amol Laxman	<i>AL</i>
7	Bombale Akshay Mahadev	<i>AM</i>
8	Chambhare Pranay Ashokrao	<i>CA</i>
9	Chopde Tejas Vinayak	<i>TV</i>
10	Choudhary Bhakti Yatin	<i>BY</i>
11	Chougule Anish Ajay	<i>AA</i>
12	Damdhare Purushottam Samadhan	<i>DP</i>
13	Das Siddhant Biswa Bhushan	<i>DB</i>
14	Dayal Mayur Vilasappa	<i>DM</i>
15	Bhatt Aakanksha Ravikumar	<i>AR</i>
16	Pophale Satish Badrinath	<i>PS</i>
17	Raut Akshay Ramesh	<i>AR</i>
18	Relekar Vipul Vishnu	<i>VR</i>
19	Bhosle Amol Laxman	<i>AL</i>
20	Ringane Sakshi Rambhau	<i>SR</i>
21	Vivek Kumar	<i>VK</i>
22	Walunj Pratiksha Arjun	<i>WP</i>
23	Yesage Santosh Mahadev	<i>YS</i>
24	Pandele Swati Ravindrakumar	<i>PS</i>
25	Patil Shubham Motiram	<i>PM</i>
26	Pophale Satish Badrinath	<i>PS</i>
27	Raut Akshay Ramesh	<i>AR</i>
28	Relekar Vipul Vishnu	<i>VR</i>
29	Chambhare Pranay Ashokrao	<i>CA</i>

Sr.No	Name Of Participant	Sign
30	Pathak Hitesdra Manohar	<i>PH</i>
31	Deshmukh Shrirang Kalyanrao	<i>DS</i>
32	Dhamangave Shradha Shesherao	<i>DS</i>
33	Dorle Badri Ramkisan	<i>DR</i>
34	Endole Anand Madhav	<i>EA</i>
35	Furange Shyam Suresh	<i>FS</i>
36	Gupta Amit Kumar	<i>GA</i>
37	Ingle Umeshi Suresh	<i>IS</i>
38	Kadam Akash Pandurang	<i>KA</i>
39	Kharabe Yogesh Ravindra	<i>YR</i>
40	Kinholkar Nikhil Vinayak	<i>KN</i>
41	Kokane Dinesh Pandurang	<i>KK</i>
42	Urade Shubham Sanjay	<i>US</i>
43	Ushir Kalpesh Kailas	<i>UK</i>
44	Utakar Devash Pandurang	<i>UD</i>
45	Vanakade Prathamesh Prabhakar	<i>VP</i>
46	Aher Keshav Bharudas	<i>AK</i>
47	Baddiya Bhushan Ramesh	<i>BR</i>
48	Bhakte Mukta Hanmantrao	<i>BM</i>
49	Bhatt Aakanksha Ravikumar	<i>AR</i>
50	Gupta Amit Kumar	<i>GA</i>

Mrs. P.A. Kamble
Coordinator

Sinhgad Institute of Technology, Lonavala

Two Days Workshop on

BASICS OF INTELLECTUAL PROPERTY RIGHTS

Intellectual property is a key aspect
for economic development.

16-17 December, 2016

09:30 - 05:00

• @ IT Seminar Hall

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Mr. Pratik Hendre

Examiner of Patents and Designs

For Registration Contact:

Dr. Manohar Chaudhari

+91 92843 17762

Facts of Research Methodology

DATE : 07- 08TH AUG 2016
TIME : 10:00 - 04.00 A.M.
LOCATION : IT SEMINAR HALL

Contact- 99212 72469



Sinhgad Institutes

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SS

WORKSHOP ON ENTREPRENEURSHIP SKILLS, ATTITUDE & BEHAVIOUR DEVELOPMENT



Date : 07- 08th June 2016
Time : 10:00 - 04.00 a.m.
Location : IT SEMINAR HALL

CONTACT-
99212 72469

All attendees must
bring their IDs.