List of supporting documents 2017-18

Sr. No.	Name of the workshops / seminar
1	workshop on "Data Analytics and Research Methodology."
2	Workshop on "Research: How & When"
3	Seminar on "Quantitative Research Methods".
4	Seminar on, "Bridging the gap between Industry and Academia" For Entrepreneurship Development
5	INTELLECTUAL PROPERTY RIGHTS DAY
6	Intellectual Property and Rights with Patents
7	Significance of Intellectual Property and Rights
8	IPR Awareness Programme for Industry & Academia
9	Seminar on, "Innovation and Entrepreneurship Development Centre"

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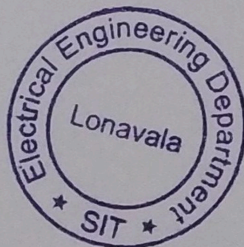
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Lonavala, Pune, 410401, Website : sit.sinhgad.edu

Department of Electrical Engineering

Sinhgad Institutes

Date: 19 April, 2018

NOTICE

Interested faculties and students are invited for workshop on **"DATA ANALYTICS AND RESEARCH METHODOLOGY."** Workshop will be organized on 24 April 2018, SIT, Lonavala. Interested faculties should contact to event co-ordinator Mr. M.N. Kalgunde on or before 23 April, 2018.

HOD

Department of Electrical Engineering.

Brochure:

Department of Electrical Engineering

DATA ANALYTICS AND RESEARCH METHODOLOGY

Event Details	

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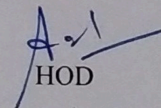Lonavala, Pune, 410401, Website : sit.sinhgad.edu

Department of Electrical Engineering

Event Name	DATA ANALYTICS AND RESEARCH METHODOLOGY
Date	24/04/2018
Time	11 AM to 1 PM
Coordinator	Mr. M.S. Shinde
Resource Person	Prof. R.S. kavitkar, Professor E&TC Engineering, SCOE, Pune
Type	Workshop
Total Participants	20
This workshop was useful to faculties and UG and PG students as well. Students get insights of research methodology.	

Photo:

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Department of Electrical Engineering

Session going on

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Department of Electrical Engineering

Attendance:

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Department of Electrical Engineering

Event Attendance

Name of the Event	Faculty Development Program on "DATA ANALYTICS AND RESEARCH METHODOLOGY"
Date	24 April 2018

Sr.No	Name of Participant	Sign
1	M.K.Bhosle	
2	D.N.Duche	
3	T. Adikane	
4	A.A.Maske	
5	S.S.Shinde	
6	P.M.Linge	P.M. Linge
7	S.P.Mahangade	
8	P.S.Mhehtre	
9	M.S.Shinde	
10	A.V.Tamhane	
11	K.S.Karnekar	Karnekar.k.s
12	F.S.Ghodichor	
13	S.N.Mali	
14	S.R.Borate	Borate.s.r
15	A.P.Udawant	
16	V.S.Kadam	
17	M.V.Chavhan	
18	S.A.Bhad	S.A.Bhad.
19	S.G.Pandit	
20	S.V.Tade	Tade

Date: 6th Oct, 2017

NOTICE

All faculties are invited to attend Workshop on **"Research: How & When."** Workshop will be organized at 11th Oct 2017, Time 10.00 AM to 12.00 PM. Venue IT seminar Hall. SIT, Lonavala. Interested faculties and students should contact to event coordinator Mrs. Pallavi Ahire, before 10th Oct, 2017.

HOD

Department of Information Technology

Sinhgad Institutes

Department of Information Technology

Organizing Workshop

On

"Research: How & When"

DATE: 11/10/2017
DAY: Wednesday
VENUE: IT seminar Hall.
SIT, LONAVALA
TIME: 10:00AM TO 12:00PM

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Department of Information Technology

Report- Workshop on "Research : How & When"

Department of Information Technology has organized one day workshop.

Event Name: "Research: How&When"

Date: 11th Oct, 2017.

Time: 10 AM To 12 PM

Venue: **IT seminar Hall**

The resource person for the workshop: Dr. T. J. Parvat. Professor Computer Engineering.

Total 26 participants have attended the workshop.

This workshop is aimed to provide purposes of research. Individual teachers can benefit by understanding meaning and objectives of applied research.   Research Methods versus Methodology & Significance of Research and Process and Importance of knowing how research is done.

Dr.T.J.Parvat Delivering Session

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Department of Information Technology

Attendance :

SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

DEPT.- Information Technology

Name of the Event :-Workshop on "Research: How & When"

Date :- 11/10/2017

Sr.No	Name of Participant	Sign
1	PRATIK DESHMUKH	
2	PALANDE SHUBHAM SAMIR	
3	APARNA THAKUR	
4	SUYASH CHITRANSHI	
5	YEDE KIRTI HANUMAN	
6	SHIRUDE PUSHPAK DILIP	
7	KASHID ANIL JAYSINGRAO (DA)	
8	CHIKHALE PALLAVI RATNAKANT	
9	MITKARI SHUBHAM	
10	PATHAK HITENDRA MANOHAR	
11	SONWANE SUSHANT GOUTAM (DA)	
12	KHEDKAR AMOL BHIVSEN	
13	VORA ANKIT MAYUR	
14	RISHABH GOEL	
15	DARGUDE SHREYAS ATMARAM	
16	JOSHI VARAD RAMESH	
17	RAMPHALE AISHWARYA	
18	BORSE NEELAM RAJARAM	
19	SMIT TOPIWALA	
20	BHAGAT KOMAL RAJESH	
21	KHATRI ILESH HARBANS SINGH	
22	KUNAL MAHAJAN	
23	MAGDUM VIKAS VASANT	
24	DEO PRITESH SHARAD	
25	MANOHAR CHAUDHARY	
26	SHUBHANGI PATIL	

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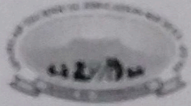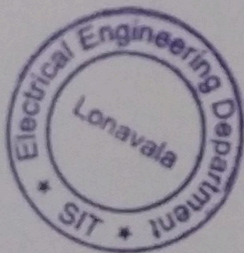
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Department of Electrical Engineering

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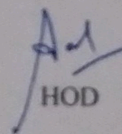
Date: 24 July, 2017

NOTICE

All faculties are invited to attend Seminar on **"Quantitative Research Methods"**. Event will organized at EE Seminar Hall on 27 July 2017, Time 11 AM to 2 PM, Venue: Electrical Seminar Hall.SIT, Lonavala. Interested faculties and students should contact to event co-coordinator Mr. Sarita Tayade, Assistant professor, Electrical Engineering department, before 26 July, 2017.

HOD

Department of Electrical Engineering.

Brochure:

Department Of Electrical Engineering
Organizing

SEMINAR ON

"QUANTITATIVE RESEARCH METHODS"

27JULY2017

11.00AM TO 2.00PM

VENUE: Electrical Seminar Hall

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Department of Electrical Engineering

Report- Seminar on "Quantitative Research Methods"

Department of Electrical Engineering has organized one day seminar on

Title: "**Quantitative Research Methods**"

Date: 27 July 2017, Time 11 AM to 2 PM,

Venue: Electrical Seminar Hall.SIT, Lonavala**.**

The resource person: Dr. Parikshit Mahalle, HOD, SKNSIT Wadgaon.Pune.

Total 31 participants have attended the Seminar.

Seminar was aimed to provide insights of research methods. Individual teachers can benefit by understanding meaning and objectives of applied research. Research Methods versus Methodology & Significance of Research and Process. Importance of knowing how research is done.

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Department of Electrical Engineering

Session Going on

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Department of Electrical Engineering

Attendance: 31

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Department of Electrical Engineering

Event Attendance

Name of the Event	Seminar on "QUANTITATIVE RESEARCH METHODS"
Date	27 JULY 2017

Sr.No	Name of Participant	Sign
1	BHUSKADE ASHWINI UDAYRAO	
2	KHEDKAR AISHWARYA VIJAY	
3	AVACHAT HARISH SUNIL	
4	SADU TIRUMALA RAD VASUDEORAO	
5	KAMBLE RACHITA DILIP	
6	KHUNDARE AVINASH R	
7	PRIYA MEHRA (J K K)	
8	MESHRAM SUMEET S	
9	MHAISNE PRANALI P	
10	MOON RUSHIKESH DILIP	
11	MUNDHE DATTATRAY B	
12	NAGE AKASH RAJENDRA	
13	NIKAM NEHA SATISH	
14	PATIL DEVENDRA ANAND	
15	PATIL SIMRAN MAHENDRA	
16	KAVIKANT RAI	
17	SATHE MAYUR MADANRAO	
18	SAWALE NITIN VILAS	
19	SHAHANE SHUBHAM KACHARUDAS	
20	SHELKE JAYENDRA SUBHASH	
21	SHINDE DNYANESHWAR MAROTI	
22	SHUKLA ANKIT	
23	SINGANJUDE SAURABH MAHADEO	
24	SIRSATH DATTATARY KISHAN	
25	WADBUDE VRUSHALI NANDKISHOR	
26	WAGHMODE SURAJ BHAGWAN	
27	ANUJIT AVINASH SAWANT	
28	LALIT D THAKRE	
29	RAKESH MORANE	
30	DEOKATE PRAFULL	
31	SANDEEP R RATHOD	

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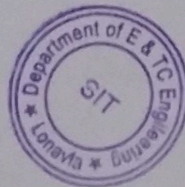### Department of Electronics and Telecommunication Engineering

Sinhgad Institutes

Date: 18 July 2017

NOTICE

All faculties are invited to attend Seminar on, **"Bridging the gap between Industry and Academia"** The seminar will be delivered on 22 July 2017, event is organized at F-126 (Electrical seminar Hall), SIT, Lonavala.Interested faculties and students should contact to event co-coordinator Prof N.B. Shitole before 21 July 2017.

HOD

Brochure:

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SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA
AFFILIATED TO SAVITRIBAI PHULE PUNE UNIVERSITY
Phone: 02114- 673257,355,356

DEPARTMENT OF ELECTRONICS AND TELECOMMUNICATION ENGINEERING

Organizing Seminar on

"**Bridging the Gap**

Between

Industry and Academia"

DATE: 22 Jully 2017
TIME:10:00 AM To 1.00 PM

VENUE: F-126. Electrical Seminar Hall,

Dept Tel.:+91 2114-673401, Office : 02114 673355 ,673356, email:hodentc.sit@sinhgad.edu, Website: sit.sinhgad.edu

Sinhgad Technical Education Society's

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Lonavala, Pune, 410401,Website :sit.sinhgad.edu

Department of Electronics and Telecommunication Engineering

EVENT :-

Title:	**"Bridging the gap between Industry and Academia "**
Date:	22 July 2017
Day:	Saturday
Venue:	F-126 (Electrical seminar Hall), SIT, Lonavala.
Time:	10am to 1pm
Staff Co-coordinator:	Prof N.B.Shitole
Event Coordinator:	Entire IEEE team
Event Conducted Under:	IEEE STUDENTS CHAPTER
Activity Type:	Seminar
Target Group:	All SE, TE & BE Students

Brief Summary about Activity:

A seminar on "**Bridging the gap between Industry and Academia**" was organized by IEEE Student Branch, SIT, Lonavala, for all the Students of SIT, Lonavala. The speaker for the seminar was Mr.Kaustubh Bhadbhade and the aluminis were Mr.Moin Moosa and Mr.Samruddha Shete. Around 53 students attended the session. Prof. N.B.Shitole was the staff coordinator and all the members of IEEE Student Branch, SIT Lonavala were the student coordinators for the event.
The Event was a great success.

Photographs of Activity:

Inauguration

Session started

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Department of Electronics and Telecommunication Engineering

Attendance:

Sr.No.	Name	Sign
01	Ishani Aryan	Hardy
02	Sneha Dengan	Sneha
03	Jadhav Prajkta B-18	Prajkta
04	airam peje B-14	Prajuu
05	TE-B- Pawars mohini 23	Pawar
06	TE-Bkalyankar Harshada L. 13	
07	c-34 Muthaiya Amuda	dy
08	C-32 Pagcel Mesham	Pmeham
09	D-15 Kantalavar Nikita 15	
10	c-23 kamse yogesh Maghyar	H958
11	C-24 kamande Rushikesh Angad	
12	C-67 Kadu Gaurov O.	
13	C-63 sushant Gavade	
14	C-70 Shubham D. Yeul	
15	C-68 mhetre mahesh Gempatrao	
16	TE-B-66 sudagkun P shinde	shn
17	TE-C 45 Sa(W)	
18	TE-B Shevale surad D.	
19	TE-B lakkas Ackash	
20	TE-B Nayan Sutgashe	
21	TE-C27Kalyankar Ganesh	
22	TE C28 Kulkarni Bhagysh	
23	TE C-18 Vivek Jadhav	Vivek

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24	Madgavas Sidheshwar	
25	Abhishek Ambure	
26	TE-B2 Gawade Mangesh	Mangesh
27	TE C-2 Vikas Kokane	Kokane P
28	SE-A Rishabesh Kumar	Ravikam
29	SE-A PRITHVI KARKERA	
30	SE-A Pranay Gaikwad	
31	SE-A-2 Abhijeeth NR	Deepak
32	SE-A17 Deepak Hirugve	Nikhil
33	SE-A-26 Nikhil Mishra	
34	SE-A 35 Sudheer Kumar	
35	SE A-02 Abhinav Narayan	Abhinav
36	SE A-45 Zawed Ansari	Zawed
37	SE A-20 Pranshu Kumar	Pranshu
38	SE A-02 Aditya Raj	Aditya Raj
39	SE A-44 Tashu Ke. Sharma	Tashu
40	SE A-33 Shardul Srivastava	shardul
41	SE A-28 Hrishikesh Pawar	
42	SE A-07 Mahendra S Bhamre	
43	SE-A-13 Param D Dudhe	
44	TE-B-67 Pimpple Vinod	
45	S.E-A-04 Ainlawar Kamesh	Kamesh
46	TE C-42 Pawan Rj	Rawy
47	-11- -08 Shardha B	
48	TE C-46 Patil Ajay	
49	TE-C-62 Swapnil Dhakade	
50	TE-A-55 Sanket Yeole	Yeole
51	TE-C-44 Rajat Sharma	Sharma

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Sr.No.	Name	Sign
	SE(A-22)	
52	SE A -22 Kota lalita Bhaskar	
53	T.E. c - 04 Anhay Jain	
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Sinhgad Technical Education Society's

Sinhgad Institute of Technology, Lonavala

Two Days Workshop for

INTELLECTUAL PROPERTY RIGHTS DAY

02-03 January, 2018
09:30 - 04:30
@ Mechanical Seminar Hall

For Registration Contact:

Dr. Manohar Chaudhari
+91 92843 17762

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**Department of Computer Engineering**

Date: 5 FEB, 2018

NOTICE

All faculties are invited Two days workshop on **"Intellectual Property and Rights with Patents"** event will be organized at 26-27 Feb 2018, SIT, Lonavala**.** Interested faculties and students should contact to event coordinator Mr. P. V. Raut, Assistant Professor, **Computer Engineering** Engineering Dept, before 20 Feb, 2018.

HOD

Department of **Computer Engineering**

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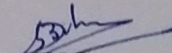Gat No. 309/310 ,Kusgaon (Bk), off Mumbai –Pune, Expressway.
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Department of Electrical Engineering

Date: 10 July, 2017

NOTICE

All faculties are invited Two days workshop on **"Significance of Intellectual Property and Rights"** event will be organized at 16-17 July 2017, SIT, Lonavala**.** Interested faculties and students should contact to event coordinator Mr. M. S. Shinde, Assistant Professor, Electrical Engineering Dept, before 15 July, 2017.

HOD

Staff Coordinator   Department of Electrical Engineering
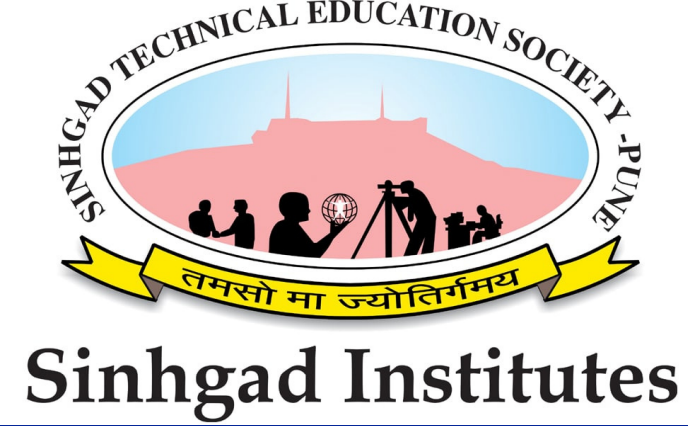
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Sinhgad Institute of Technology, Lonavala

IPR Awareness Programme for Industry & Academia

Patents need inventors more than inventors need patents

Time :   9:00 – 04.00

Date: 17–18 Feb 2018

Any queries contact:
8862046732

Registration Link:
https://forms.gle/sR38LyGLct2HmAoh7

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Sinhgad Institutes

Date: 19thJuly 2017

NOTICE

All faculties are invited to attend Seminar on **"Innovation and Entrepreneurship Development Centre"**. The seminar will be delivered on 21st July 2017 at **Electrical Seminar Hall**. Interested faculties and students should contact to event co-ordinator Mrs. Shaileja Lohar before 20thJuly, 2017.

HOD

Department of Computer Engineering

Brochure:

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Department of Computer Engineering

Event Name   : **Innovation and Entrepreneurship Development Centre**
Day &Date   : 21 July 2017, Friday.

Time   : 11.00 to 2.00 PM

Venue: Electrical Seminar Hall

Resource person: Mr. K. T. Talele

The event "**Innovation and Entrepreneurship Development Centre**" organized by Computer Engineering, Sinhgad Institute of technology, Lonavala. It was started at **11.00am** on **21st July, 2017**.

The event was organized at **Electrical Seminar Hall**.

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Gallery:

| Mr. K. T. Talele with Dr D. D. Chaudhari | Discussion with faculties |

Attendance:

Sinhgad Technical Education Society's

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SINHGAD INSTITUTE OF TECHNOLOGY, LONAVALA

Seminar

Name of the Event :-Seminar on "Innovation and Entrepreneurship Development Centre".

Date :- 21/07/2017

Sr.No	Name of Participant	Sign
1	Nakul Sharma	
2	Shobha Sanjay Raskar	
3	Dhanashree Ajay Phalke	
4	Rupali Borse	
5	Dinesh Ghorpade	
6	Ms. Archana Vishnu Ugale	
7	Ankita Vijay Raut	
8	Yogesh Sunil Jorwekar	
9	A.v.sagare	
10	Pratibha Kashid	
11	Kopal Gangrade	
12	Mr. Anand D. Dhawale	
13	Pradnya Bachhav	
14	Pallavi Vitthalrao Kulkarni	
15	Neha Amit Rai	
16	kapil Dnyaneshwar Dere	
17	Rahul Subhash Gaikwad	
18	Sumit Suryakant Dhotre	
19	Shikha Pachouly	
20	Kalyani Pramod Mule	
21	Priyanka Nitish Kate-deshmukh	
22	Khatal Sunil Sudam	
23	Dattatray Sahebrao Shingale	
24	ANURADHA DEOKAR	
25	RAHINJ P.L.	
26	Manisha Ravindra Parkhe	
27	Sanjay B.Waykar	
28	Sharad Raghunath Deshmukh	
29	Anil M.Bhadgale	
30	Deepali Jawale	
31		
32		
33		
34		
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