



SINHGAD TECHNICAL EDUCATION SOCIETY'S
SINHGAD INSTITUTE OF TECHNOLOGY

(Affiliated to Savitribai Phule Pune University, Pune & Approved by AICTE)
Sinhgad Institutes Gat No. 309/310, off Mumbai Pune Expressway Kusagaon (Bk), Lonavala Pune – 410401
Department of Electronics and Telecommunication Engineering

TITLE: Introduction to Patent filing in India

Date: 28July2018

Day: Saturday

Venue: Seminar Hall (MECH.Dept)

Time: 9:00amto11:00Am

ActivityType: Institute level Seminar for students.

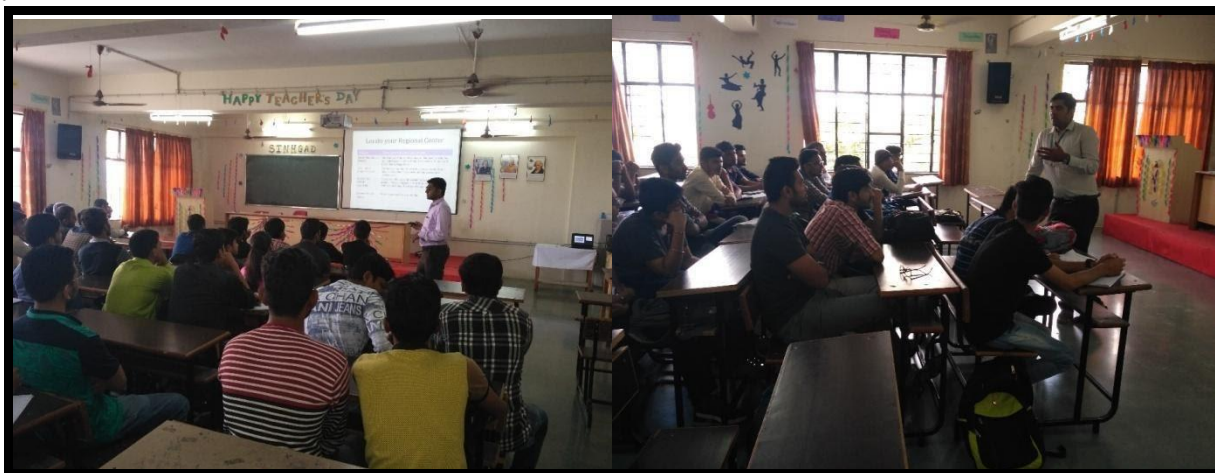
TargetGroup: ALLSTUDENTSOFSTESLONAVALACAMPUS

BRIEFSUMMARYABOUTACTIVITY:-

The session was conducted by Mr. A. A. Satpute and Mr. A. R. Gurudasani. The presentation mainly to create awareness about Patent filing procedure in India. In presentation the focus was on 'How much the procedure is easy to file a patent in India'. In this we have discussed about the methods of filing patent, the cost required, location of patent offices, finding of patents, which can be and can't be file as patent etc.The session was conducted for one and half hr.in interaction type. Mr. A. R. Gurudasni has explained in nice manner about patent filing documentation, in this he has explained about the different forms required to file patent and technical writings.

We ended the session with conclusion that 'the Patent filing is easy but to invent something is quite difficult'

Gallery:



Session speaker Mr. A. A. Satpute

Session speaker Mr. A. R. Gurudasani



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Department of Electronics and Telecommunication Engineering

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7.	Dipak Kate	SIT-mech (BE)	[Signature]
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10.	Abhisek Mali	-	[Signature]
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12.	Shivamkrishna	SIT-ENTC(SE)	[Signature]
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