

Faculty Profile



1. Name: **Mr. Vikram M. Chavan**
2. Designation: Assistant Professor, Computer department,
3. Institute: Sinhgad Institute of Technology, Lonavala
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5. Date of Joining: 2 July, 2012
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9. Research Interests: **VLSI & Embedded system.**

Educational Details:

- **M.E.** (VLSI & Embedded system), SIT, Lonavala, Pune, First class, Dec 2012(Pune uni.).
- **B.E.** (E&Tc) from SVPM,s College of Engineering, Malegaon(Bk),Baramati, First class with distinction, June 2009(Pune Uni.)
- **H.S.C.**from, Kolhapur Board First class, April 2004.

Experience: (09 years)

2012 till date: - Working as a **Assistant Professor** in Computer Dept, Sinhgad Institute of Technology, Lonavala, India.

Responsibilities:

- Departmental TG Co-ordinator from 2017-2020.

Research publications:

International Journals: **04**, National Conference: **02**

Achievements and Awards:

- Worked as a **Session Organizer in “ePGCon (2008)-14”** at SIT, Lonavala.(22th Nov 2014)
- Worked as **“Faculty co-ordinator of the Logistic Committee”** for the national level Technical conference “TECHTONIC-2014”, at SIT Lonavala. (10th feb. 2014)
- Worked as **“Faculty Coordinator for Industrial Automation Training Workshop (PLC)”**, at SIT Lonavala.(15 March 2015)
- Worked as a **Coordinator** for two day state level STTP on **“WSN fundamentals & protocol design using NS-2 & NS-3”**sponsored by University at SIT Lonavala.(23rd -24th Jan. 2016)
- **“Award of Excellence”** for excellent contribution in teaching Learning Process at Institute **A.Y.:2012-13. (5th sep. 2013)**
- Worked as **“Faculty co-ordinator of the Event 3D Rubiks Cube”** for the national level Technical conference “TECHTONIC-2019”, at SIT Lonavala. (20-22th feb. 2019)
- Worked as a **Session coordinator in “6th I2IT-IEEE Bombay section”** (2April -4 April 2021)
- Worked as a **Publication chair in “TKD-21-IEEE SB”** SIT, Lonavala.(30 May 2021)

Chavan

Place: Lonavala

Mr. Vikram M. Chavan