Profile

- 1. Name: Dr. Vijaya Gopalrao Rajeshwarkar
- 2. Designation: Assistant Professor, Department of E&TC
- 3. Institute: Sinhgad Institute of Technology, Lonavala
- 4. Email: vijaya.rajeshwarkar.sit@sinhgad.edu; v.rajeshwarkar@gmail.com
- 5. Date of Joining: June 15, 2010
- 6. Mobile: +91-9923103439/+91-9423292099 (whatsapp)
- 7. Date of Birth: 23rd July, 1972
- 8. Address: 2BHK, Flat No.03, STES Campus, Lonavala

9. Research Interests: TV White Space, Cognitive Radio, Wireless Sensor Networks

Educational Details:

- Ph.D. (Wireless Communication), Jawaharlal Nehru Technological University, Hyderabad, 2023
- M.Tech. (EXTC), Dr.Babasaheb Ambedkar Technological University, Lonere, First class (64. 38%),2006 ٠
- B.E. (ETC), TPCT's College of Engineering, Osmanabad, First class (61.42 %), 1993
- H.S.C (Maharashtra State Board of Secondary & Higher Secondary Education), Ramkrishna Pramhanmsa College, Osmanabad, Distinction (80.17%), 1989
- S.S.C (Board of Secondary Education, Andhra Pradesh), Vivek Vardhini Kanya School, Hyderabad, First class (76.83%), 1987

Experience: (27 years)

- 2010 to till date: Assistant Professor E&TC Dept., Sinhgad Institute of Technology, Lonavala, India
- 2006 to 2010: Lecturer, Government Polytechnic, Osmanabad.
- 2003 to 2006: Lecturer, Government Polytechnic, Ratnagiri.
- 1996 to 2003: Visiting Lecturer in Electronics, Government Polytechnic, Osmanabad.
- 1996 to 1997:- Visiting Lecturer in Electronics, Government Residential Women's Polytechnic, Latur

Academic Recognition

✓ Recognized as UG Teacher from February 12, 2014 to Till Date

Subjects Taught:

- Network Theory
- Integrated Circuits
- Broadband Communication System
- Fiber Optic Communication

Responsibilities Handled:

- Anti-ragging committee member, SIT, Lonavala •
- Girls hostel discipline committee member Institute Level •
- Mentor-Mentee (TG) Coordinator Institute & Department Level •
- NBA, NAAC Committee member- Department Level •
- **Time Table Incharge** •
- **BE Project Coordinator**
- PBL Coordinator Department Level •
- ISTE Club Coordinator Department Level •
- **Class Coordinator** •
- Lab Incharge

Research publications:

- International Journals: 10, National Journal: 01, International Conference: 09, National Conference: 01, Book Chapters: 01
- Google Citation: 07, h-index: 01, i10 index: 00.

Professional Membership:

Life Member of the International Society for Technical Education. (LMISTE 130265)

Books Published:

 \geq Published Text book on "Electrical Circuits" for S.E. (E&TC) as per SPPU syllabus.



- **Electrical Circuits & Machines**
- **Electrical Circuits**
- Modernized Internet of Things
- **Linear Integrated Circuits**

Achievements and Awards:

- Session incharge for the First International conference "GISFI", at SIT Lonavala.
- Session incharge for the CPGCon organized at SIT, Lonavala
- * Resource person in TTT arranged by Sinhgad Institutes.
- Reviewer for various national & international conference.

Vajohwate

Place : Lonavala

Dr. Vijaya G. Rajeshwarkar