Faculty Profile

Name: Dr. Dnyaneshwar S. Mantri
Designation: Professor, Department of E&TC

3. Institute: Sinhgad Institute of Technology, Lonavala

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5. Date of Joining: July 3, 2006
6. Mobile: +91-9922431612
7. Date of Birth: 03rd Sept. 1970

8. Address: 2BHK, Flat No.09, STES Campus, Lonavala

9. Research Interests: Wireless Sensor Networks, Wireless Networks, Adhoc Networks, VANAT

Educational Details:

- PhD. (Wireless Communication), Aalborg University, Denmark, 2017
- M.E. (Electronics), Shivaji University, Kolhapur, First class (63. 87%),2006
- B.E. (Electronics), Shivaji University, Kolhapur, First class with Distinction (66 %), 1992
- H.S.C (Pune Board), Walchand college of Arts and Science, Solapur, Distinction (81.67%), 1988
- S.S.C (Aurangabad Board), Janata Vidya Mandir, Murud, First class (72.28%), 1986

Experience: (26 years)

- 2018 to till date:-Professor, E&TC Dept., Sinhgad Institute of Technology, Lonavala, India
- 2010 to 2018: Associate Professor E&TC Dept., Sinhgad Institute of Technology, Lonavala, India
- 2006 to 2010: Assistant Professor E&TC Dept., Sinhgad Institute of Technology, Lonavala, India.
- 1998 to 2006:- Lecturer in Electronics and Telecommunication Engg. Dept., STBT, Tuljapur.
- 1996 to 1998:- Lecturer in Electronics and Telecommunication Engg. Dept., MGM's COE, Nanded,
- 1993 to 1996:- Lecturer in Electronics Dept. at M.C.E. Nilanga, MS. India

Responsibilities Handled:

- Chairman , Library Committee, SIT, Lonavala
- Member of IPR Committee College Level
- Member of Research Review Committee- Department
- General Co-chair for IEEE Conference GCWCN2014 and GCWCN2018
- NAAC Pear Team In-charge [2016-17]
- Head of E&TC Department. [2006, 2014-15]
- NBA Coordinator SIT, Lonavala [2009-2013]
- Member of Purchase committee [2007-2012]
- QAP Program In charge for the Department
- Chairman of FE admission Committee [2007,08,09]
- Coordinator for STTP and FDP on NS-2, NS-3 and Latex Workshop

Research publications:

- International Journals: 14, National Journal: 03, International Conference: 26, National Conference: 02,
- Book Chapters: 05
- Google Citation: 265, h-index: 08, i10 index: 07 (As on 25th Nov.2021)

Professional Membership:

- ↓ Life Member of International Society for Technical Education. (LMISTE 51293.)
- Life Member of IETE (M215186.)
- Member of IEEE (92496577)

Books Published:

- A Practical Approach to Electronic Circuit Design
- Microcontrollers: 8051 and PIC
- Proceeding of IEEE Conference GCWCN2014
- Proceeding of IEEE Conference GCWCN2018

Achievements and Awards:



- Recognized as PhD Guide in SPPU University
- Session Chair, in reputed Conferences like IEEE, Springer, EPGCON etc
- Series Editor of E-book, STM Journals.
- Guest editor for Wireless Technology Journal
- Guest Editor For Special issue of GWS-2015, WPC Springer Journal
- One of the paper has 200 downloads
- Becomes TPC and Session chair at ICACCI2013, 2014 and 2015 conference., ICPC2015,
- Reviewer of renowned Journal and conferences, [Springer, Elsevier, IEEE, Sensor Journal, Pervasive computing Journal, Ad-hoc Networks, Athes- Journal]
- Subject Chairman and Paper setters of University of Pune, Solapur University and Shivaji University.
- Member of LIC Committee University of Pune.
- Elected as Board of Studies Member, Dr. BAMU Aurangabad

Place : Lonavala Dr. D. S. MANTRI

Important Link

Google Scholar: https://scholar.google.com/citations?user=pAx-Uz8AAAAJ&hl=en

Linkedin: https://www.linkedin.com/in/dnyaneshwar-mantri-7b0b29140/

ORCID: <u>Dnyaneshwar Mantri</u> (0000-0001-6778-9024) - ORCID | Connecting Research and Researchers

Publon: https://publons.com/researcher/1197463/dnyaneshwar-s-mantri/

Web of Science Researcher ID: <u>AAA-9291-2021</u>

Research Gate: https://www.researchgate.net/profile/Dnyaneshwar-Mantri